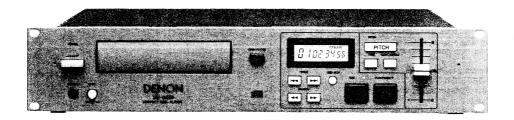
Hi-Fi Component

DENON

SERVICE MANUAL MODEL DN-650F

CD PLAYER

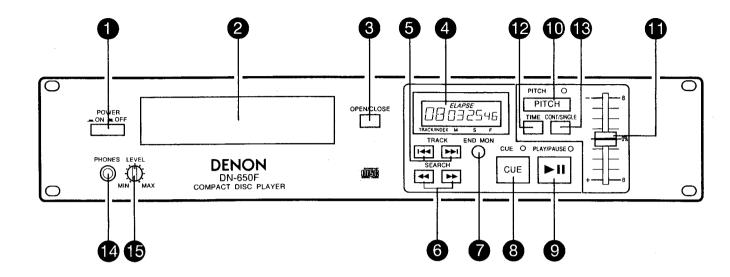


- TABLE OF CONTENTS -

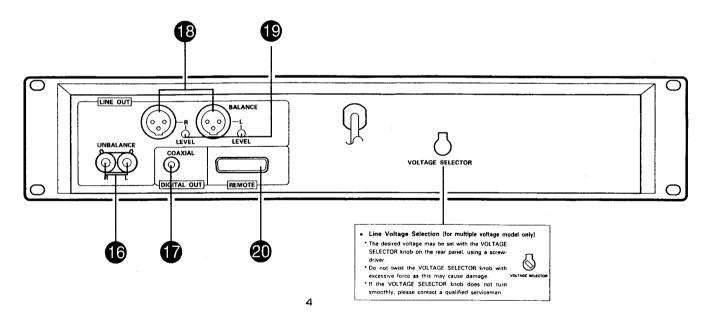
~ 8
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NIPPON COLUMBIA CO., LTD.

FRONT PANEL FRONTPLATTE PANNEAU AVANT PANEL FRONTAL FRAMSIDA



REAR PANEL RÜCKWAND PANNEAU ARRIERE PANEL TRASERO BAKSIDA



(1) Names, Dimensions and Functions of the Parts

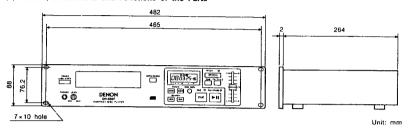


Figure 1

POWER (Power Switch)

Switches the power of the unit.

2 Disc Holder

The disc is placed on this holder. Pressing the disc holder open/close button ③ will open and close the holder.

When loading the CD, place it securely in the disc holder.

OPEN/CLOSE (Disc Holder Open/Close Button) Press to load or eject the disc. Each press will open or close the disc holder .

4 Time Display

This display shows the track or index number, time and elapsed or remaining time. Each frame represents 1/75 of a second.

TRACK (Track Button)

This button selects the track or index to be played.

Refet to Page 11, 15

6 SEARCH (Search Buttons)

These buttons are used to accurately change the positions where disc play will start.

Refet to Page 14

1 END MON (End Monitor Button)

This button is pressed during the standby mode to play the last section of the track.
Refet to Page 15

3 CUE (Cue Button)

Pressing the CUE button during play provides a return to the position at which play was started. Pressing the PLAY/PAUSE button and the CUE button alternatively allows the CD to be played from the same position any number of times. The red CUE LED will blink from the time the CUE button is pressed until the CD has reset to the position at which play was started. Steady lighting of this LED indicates the ready condition.

Refet to Page 13

PLAY/PAUSE (Play/Pause Button)

Each press of the PLAY/PAUSE button causes the operation to change from play to pause or from pause back to play.

PITCH (Pitch Button)

This button changes the play speed.

The pitch can be changed up to ±8% by pressing the PITCH button so the green PITCH LED blinks when moving the sliding fader.

When the playback speed is set to a value other than 0% with the preset function, the PITCH LED will light steadily to indicate that speed control is not possible from the pitch slider. Refet to Page 14

Pitch Slider

Use this slider to adjust the playback speed. Slide up to decrease the speed, down to increase the speed.

Refet to Page 14

TIME (Time Button)

The TIME button switches the time display between elapsed time and remaining time. ELAPSE or REMAIN will be shown on the display.

(B) CONT/SINGLE (Play Mode Button)

This is for switching the play mode between the single track mode (SINGLE) and continuous play mode (CONT.).
Refet to Page 11

PHONES (Headphones Jack)

Connect headphones with an impedance of 30 to 40 ohms.

I Volume Control (PHONES LEVEL)

 Use this to adjust the output level of the headphones.

D LINE OUT (Output Jacks)

RCA Pin-jack, unbalanced.

The audio is output from these jacks. Connect to the line input of the mixer. Red is for the right channel and white the left channel.

DIGITAL OUT (Digital Output Jack)

- This jack outputs digital data.
- We recommended using a 75-ohm pin cord (available in stores) for connections.

These are active balanced type outputs using XLR type connectors.

Connect them to balanced type inputs with an impedance of 600 ohms on an amplifier or console.

2) Signal layout

Pin 1 : Common

Pin 2 : Cold

Pin 3: Hot

 Applicable connector: Cannon XLR-3-11C or the equivalent

NOTE: Do not short-circuit the hot or cold pin with the common pin.

(2) Installation Precautions



The DN-650F will work normally when the player unit is mounted within 20 degrees off the vertical plane at the front panel. If the unit is tilted excessively, the disc may not be loaded or unloaded properly.

Figure 2

DN-650F 30°

Install the DN-650F to a rack so as to maintain an appropriate visual angle to read the display as shown here.

Figure 3

These adjust the level of the audio signals output from the LINE OUT L/R connectors.

REMOTE (Remote Control Connector)

(B) LEVEL L/R (Output Level Controls)

- This is a connector for parallel remote connection.
- The player can be controlled remotely with a dry contact circuit connection.
- 2) Applicable connector: 25-pin D-sub plug 3) Signal layout
- Refer to Page 19

Pin	No.	Signal	1/0	Level
1		FG	-	
	14	PLAY TALLY	0	TTL (lol=48 mA
2		PLAY COMMAND	1	HCMOS (li=-3 mA)
	15	PAUSE TALLY	0	TTL (lot~48 mA
3		PAUSE COMMAND	1,	HCMOS (li =-3 mA)
	16	CUE TALLY	0	TTL (lol-48 mA
4		CUE COMMAND	l i	HCMOS (li=-3 mA)
	17	INDEX 2/3 TALLY	0	TTL (lol = 48 mA)
5		TRACK (+) COMMAND	1	HCMOS (II 3 mA)
	18	NC	l –	
6		TRACK (-) COMMAND	1	HCMOS (li=-3 mA)
	19	NC	-	
7		SEARCH (FWD) COMMAND	1	HCMOS (li=-3 mA)
	20	NC .	-	
8		SEARCH (REV) COMMAND	3	HCMOS (li=-3 mA)
	21	NC	- 1	
9		FADER START		HCMOS
-			'	(I=-3 mA)
	22	TALLY POWER SUPPLY	0	+5 V, 20 mA
10		COMMAND COMMON	~	
	23	COMMAND COMMON	-	
11		NC	-	'
	24	E.O.M./INDEX 2/INDEX 3	0	DRY CONTACT
12		NC	-	
	25	E.O.M./INDEX 2/INDEX 3	0	DRY CONTACT
13	_ i	NC		

2 DESCRIPTION OF THE FUNCTIONS

The DN-650F is provided with special functions which are not available in common CD player. Please read the following explanation carefully before using these functions.

(1) Automatic Cueing

Automatic cuing automatically searches for sound levels which are above the preset cue detect level following track selection and sets the cue mode. This function can skip a blank portion at the beginning of a track. See Page 17.

(2) Instant Start/Delayed Start

The audio starts up within 0.01 seconds with a press of the play button. This provides an accurate start since there is no sound delay. A delayed start is also possible. The duration of the delay can be selected. See Page 17.

(3) Fade In

There will be a fade in of the sound over the selected duration when playback is started. See Page 18.

(4) EOM (End Of Message)

The EOM function causes the LCD to blink as an indication that the end of the playback is approaching when the remaining time of the playback reaches the preset time. Note that this unit may have a time deviation of 2 to 3 seconds duration. See Page 18.

(5) End Monitor

This function plays the end portion of a track. The length of playback can be selected. See Page 18.

(6) Single/Continuous Play Mode

Playback finishes at the end of the selected track in single play mode, whereas, playback continues to the end of the disc in continuous play mode. This function can be switched with the button or one of these modes can be preset after the power is switched on. See Page 11.

(7) Presetable Playback Speed

The playback speed can be fixed to a preset speed in the range of 0 to +3% of the regular speed. When the speed is preset to a speed other than 0%, speed adjustment is not possible with the pitch slider. At this time the PITCH LED will be lit steadily. See Page 18.

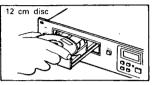
Even if the pitch slider is mistakenly moved, the preset speed will not change.

The unit also has a variety of other functions. Please carefully read Section 4 PRESET FUNCTIONS on Page 16, 17, 18 and select the desired functions.

3 OPERATION

(1) Loading and Ejecting the Disc

- Open the disc holder
 - When the disc holder is closed, press the OPEN/CLOSE button to open the disc holder.
- 2 Place a disc in the disc holder



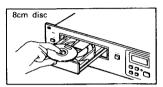


Figure 4

- 3 Loading the disc
 - Press the OPEN/CLOSE button to close the disc holder.
 - A press of the PLAY/PAUSE button will close the disc holder and start playback.
 - When a track is selected while the disc holder is open, the unit will access the selected track.

(2) Selecting Tracks

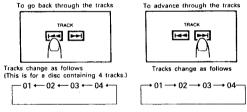


Figure 5

Each press of the TRACK button changes 1 track.

Continuing to hold the TRACK button down provides an automatic change at a higher speed which is convenient for discs that contain many tracks.

During the track selection operation, the track indication of the display will blink and the Minute, Second, Frame indication will be off.

When a new track is selected during play, after the selection operation is completed, play will immediately start from the beginning of the newly selected track.

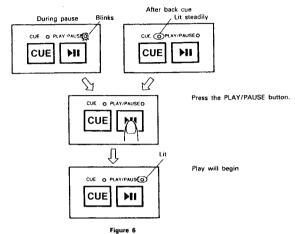
The track number can be selected before loading a disc on DN-650F.

You can select a track to play, then load a disc. DN-650F will cue up to your selected track automatically.

(3) SINGLE/CONTINUE Play Mode Selection

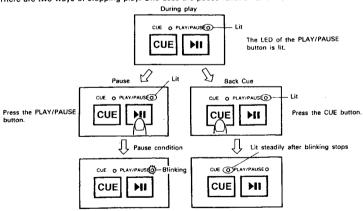
- Press the CONT/SINGLE button to set DN-650F for SINGLE track playback mode, " 5 " is displayed on TRACK section.
- During single playback mode, DN-650F stops after a specified track is played back.
- Press the CONT/SINGLE button to set DN-650F for continuous playback mode, " (" is displayed on TRACK section.
- During continuous playback mode, DN-650F continue playback until completion of playback of the last track on the disc.

Pressing the PLAY/PAUSE button during the pause condition or after the completion of back cue will start disc play.



(5) Stopping Playback

There are two ways of stopping play. One uses the pause function and the other the back cue function.



The LED of the PLAY/PAUSE button blinks.
(The CD pauses at the position where the PLAY/PAUSE button was pressed during play.

Back cue operation.
First the LED of the CUE button blinks, then it lights steadily after the operation is completed.
(The CD returns to the position where play back started from.)

Figure 7

(6) Description of the PLAY/PAUSE, and CUE Operations

- Each press of the PLAY/PAUSE button causes the operation to change from play to pause or from pause back to play.
- The play operation of this CD player is performed via DSP (Digital Signal Processor) and memory, so
 the audio starts instantly after the PLAY/PAUSE button is pressed.
- Pressing the CUE button during disc play resets the CD to the position at which play was started. (This
 is called the back cue function.)

The steps through which disc play is performed when the PLAY/PAUSE and CUE buttons are pressed are described with the aid of the following illustrations in Figures 8 through 10.

① PLAY and PAUSE

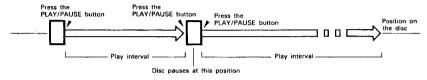
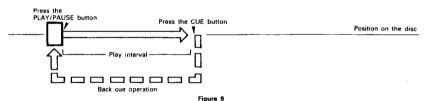


Figure 8

Pressing the PLAY/PAUSE button starts the disc play (Operation progress is shown by the arrows of Figure 8). Pressing the PLAY/PAUSE button again during disc play causes the play operation to pause, and pressing this button once more causes the disc to be played again.

2 PLAY and CUE



Pressing the PLAY/PAUSE button starts the disc play. Pressing the CUE button will reset the disc to the position where play was started. By alternately pressing the PLAY/PAUSE button and the CUE button, the disc may be played from the same position any number of times. This function is called back cue.

(3) PLAY, PAUSE, and CUE

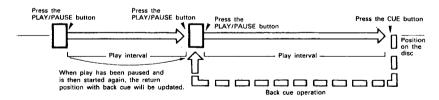
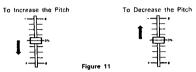


Figure 10

(7) Adjusting the Speed

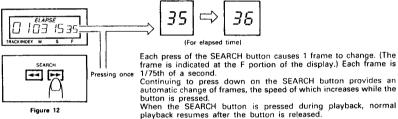
- Press the PITCH button to enable the speed to be adjusted with the pitch slider. The PITCH LED will blink to indicate that a pitch adjustment can be made.
- Pressing the PITCH button once again will light the PITCH LED and set that pitch.
- Pressing the PITCH button one further time will cause the PITCH LED turn off and the variable pitch function will switch off.
- Lowering the position of the pitch slider will increase the pitch and raising the position will decrease the pitch.



Operation of the pitch slider will not change the speed when the preset playback speed is set to a value

NOTE: Be sure to set the PITCH button to the off condition when normal speed has been set.

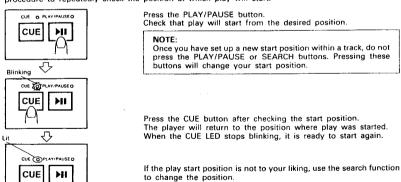
When a track is selected and the PLAY/PAUSE button is pressed, the play operation will start from the beginning of that track. However, when you want play to start from a different position, use the following procedure to find that position.



(9) Checking the Play Start Position

Figure 13

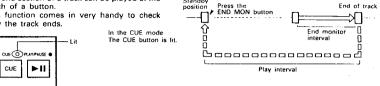
After selecting the track or after changing the play start position with the SEARCH button, use the following procedure to repeatedly check the position at which play will start.



(10) End Monitor

The end section of a track can be played at the touch of a button

This function comes in very handy to check how the track ends.



Press the END MON

The end of the track at which the CUE mode is set is played. Playing time: 0 to 35 seconds in 5-second steps can be presetting. (Refer to on page 18)

When playback ends, the pickup returns and the standby mode

CUE ►II

END MON

Figure 14

(11) Ending Playback

The operation that follows the playing of the disc to the end of the track in the single play mode, or follows the playing of the disc to the end in the continuous play mode can be selected from among the following three operations. See page 17.

① Recue

The player returns to the playback start position and the cue mode is set.

2 Next track

The player moves to the next track and the cue mode is set.

3 Stop

The PLAY/PAUSE LED blinks, the time display turns off, and the player stons. While the player is stopped, only the TRACK +/- button and the OPEN/CLOSE button are effective.

When the preset index search is set to on, the index number is displayed as TRACK/INDEX and the index can be selected with the TRACK +/- button. The operation procedure is the same as that in Section (2) Selecting Tracks.

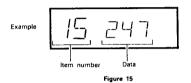
Track selection is not possible when the preset index search is set to on. To select a track, reset the index search to off.

4 PRESET FUNCTIONS

The DN-650F is equipped with non-volatile memory and is able to store various preset data. These data will not be lost even if the power is cut and so the desired settings corresponding to items such as those described in the table of Page 17, 18 can be stored to memory. Please use this function and operate the player at the optimum settings.

(1) Starting the Preset Data Change Mode

- Open the disc holder.
- Press the TIME button and the CONT./SINGLE button at the same time. The DN-650F will enter the
 preset data change mode and the first preset data will be displayed. The preset item number is
 displayed in the track display area and the current data are displayed in the minutes and seconds display
 area.



(2) Selecting the Item Number

- Press the TRACK + button to increase the item number.
- · Press the TRACK button to decrease the item number.
- The data of the selected item are displayed.

(3) Changing the Preset Data

- Press the SEARCH +/- button to select the preset data. The display data will change and blink.
- When the appropriate data have been selected, press the CUE button and store the data to the non-volatile memory.
- The display data will stop blinking.
- To change other items, repeat the operations of Steps (2) and (3).

(4) Initializing the Preset Data

- To initialize the preset data to the factory setting, store 1 to the data of item 0.
- The factory setting data are indicated by asterisks in the table of Page 17, 18.

(5) Exiting the Preset Data Change Mode

To complete the change and return to the normal mode, press the TIME button and the CONT./SINGLE button at the same time or press the OPEN/CLOSE button.

(6) Table of Preset Functions

The "*" mark indicates setting upon shipment from the factory.

ITEM	_		DISPLAY	-			DESCRIPTION
	TRA	ACK	MINUTE	SECO	DND		
Initialize	0	0			0	Non * Initial	No operation Initialize to the same data as the factory setting
Stereo/Mono	0	1-			1	Stereo * Mono	L/R stereo signal output. L/R signal output mixed.
Frame Display	0	2			0	Off	Frame is not displayed during playback.
Traine Display	Ľ				1	On *	Frame is displayed during playback.
Display Mode when		3			0	Elapse *	Elapsed time is displayed when power turned or
Power Turned On					1	Remain	Remaining time is displayed when power turned o
Play Mode when	0	4			0	Single *	Playback ends at the end of track.
Power Turned On	ļ			1	_1	Continue	Playback continues until the end of disc.
Play Lock	0	5			1	Off *	All button functions during playback. Buttons except the TIME, CONT./SINGLE, PLA' PAUSE don't function during playback.
Digital Out	0	6			0	With subcode Without subcode*	Digital signal outputs with subcode ① Digital signal outputs without subcode ②
Index Search	0	7			0	Off *	Track, Minute, Second, Frame are displayed. Track is selected with TRACK buttons.
mack obtain					1	On	Index, Minute, Second, Frame are displayed. Index is selected with TRACK buttons.
					0	No Flash	The PLAY, PAUSE, and CUE tallies in the REMO connector will remain off instead of blinking.
Tally Flash	0	8			1	Flash *	The PLAY, PAUSE, and CUE tallies in the REMO' connector will blink when the corresponding LEDs front panel blink.
Index 2/3	0	9			2	Index 2	Index 2 tally output from REMOTE connector pin 1
midex 2/3	L	,			3	Index 3 #	Index 3 tally output from REMOTE connector pin 1
					1	EOM *	EOM Tally output from REMOTE connector pins : and 25.
EOM/Index	1	0			2	Index 2	Index 2 Tally output from REMOTE connector pins and 25.
		<u> </u>			3	Index 3	Index 3 Tally output from REMOTE connector pins and 25.
Fader Start Mode	1	1			0	Start Only Start, Stop *	Player starts when fader switch turned on. Player starts when fader switch turned on, set pause mode when fader switch turned off.
					0	Recue *	When playback ends, player returns to the startingsition.
Ending Playback	1	2			1	Next Track	when playback ends, cue mode is set at the ne track.
		L.			2	Stop	Stop mode is set when playback ends.
Delay Start	1	3	1 2 3	0 0	0 0 0	0msec * 100msec 200msec 300msec	Audio start will be delayed by the selected tin setting.
	1			-	_	-00	
Core Batter Lauri	١.	١.	-	7	6	-72dB -66dB	
Cue Detect Level	1	4	-	6	0 4	-60dB * -54dB	In the cue operation following track selection, aud signals above the set level are automatically sea
		1	-	4	8	-48dB -42dB	ched and cued at that point.
			_	3	6	-36dB	

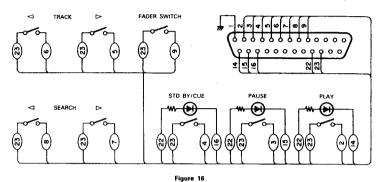
① Data other than audio data such as sub codes will be output with regular values, but the cue detect level and the fade in duration will not be the preset values, rather -∞ and 0 ms, respectively. Also, the audio start time will be 300 ms or less.

2 The cue detect level and the fade in duration will be the preset values. Data other than audio data such as sub codes will all be "0".

			DISF	PLAY					DESCRIPTION
ITEM	TRA	ACK	MIN	UTE	SEC	OND	<u> </u>		DESCRIPTION
						0	0msec	*	No fade in
Fade In Duration	1	5		1 1 1 2	1 3 5 0 4 8 4	0 0 3 6 8 5 7	10msec 30msec 53msec 106msec 148msec 185msec 247msec		The fade in operation of the selected duration is performed at the start of playback.
						0	0sec		EOM not output
EOM Duration	1	6			1 1 2 2 2 3 3	5 0 5 0 5 0 5	5sec 10sec 15sec 20sec 25sec 30sec 35sec	*	EOM (end of message) starts when the remaining time becomes shorter than this duration. The LCD blinks and the EOM tally is output.
						0	0sec		End Monitor does not function
End Monitor Duration	1	7			1 1 2 2 3 3	5 0 5 0 5 0 5	5sec 10sec 15sec 20sec 25sec 30sec 35sec	*	The selected duration at the end of the track is played back.
						0	Standard	*	Speed adjustment is possible from the front panel
Preset Playback Speed	1	8			1 1 1 1 1 2 2 2 2 2 3	2 4 6 8 0 2 4 6 8 0 2 4 6 8 0 2 4 6 8 0 0 2 4 6 0 0 2 4 6 0 0 0 2 4 6 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	+0.2% +0.4% +0.6% +0.8% +1.0% +1.2% +1.4% +1.6% +1.8% +2.0% +2.6% +2.4% +2.6% +2.8% +3.0%		The playback speed is fixed at the selected value and adjustment from the front panel is prohibited.

5 REMOTE CONTROL CONNECTIONS

To control the DN-650F remotely, refer to the example of remote control connections given below.



The rating of REMOTE connector pin 22 (TALLY POWER SUPPLY) is +5 V, 20 mA maximum. Avoid currents in excess of the rating.

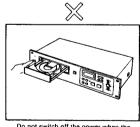
6 BEFORE SWITCHING OFF THE POWER

When you have finished using the CD player, before switching off the power be sure that the disc holder has been closed with the OPEN/CLOSE button.

CAUTION

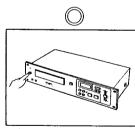
Do not forcibly close the disc holder when the power is off.

It may damage the unit when it is transported.



Do not switch off the power when the disc holder is open.

POWER OFF



Switch off the power after the disc holder has been closed with the OPEN/CLOSE button.

POWER OFF

Figure 17

GENERAL

9 SPECIFICATIONS

7 COMPACT DISCS

1. Precautions on handling compact discs

- Do not allow fingerprints, oil or dust to get on the surface of the disc.
- If the disc is dirty, wipe it off with a soft dry cloth.
- · Do not use benzene, thinner, water, record spray, electrostatic-proof chemicals, or silicone-treated cloths to clean discs.
- · Always handle discs carefully to prevent damaging the surface; in particular when removing a disc from its case or returning it.
- Do not bend the disc.
- Do not apply heat.
- Do not enlarge the hole in the center of the disc.
- . Do not write on the label (printed side) with a hard-tipped implement such as a pencil or ball point pen.
- · Condensation will form if a disc is brought into a warm area from a colder one, such as outdoors in winter. Do not attempt to dry the disc with a hair dryer, etc.

2. Precaution on storage

- player.
- protect from dirt or damage.
- Do not place discs in the following areas:
- 1) Areas exposed to direct sunlight for a considerable time.
- 2) Areas subject to accumulation of dust or

See page 11

See Page 17.

8 TROUBLESHOOTING

When you think the player might be broken, please check the following items.

The disc holder does not open or close.

- Is the power switch set to ON?
- Is the player in the process of playing a disc? ...

The display still indicates "-" when a disc is loaded.

- Is the disc loaded properly?
- . Is the disc dirty or scratched?

There is no sound or the sound is distorted.

- Are the output cables connected to the amplifier correctly?
- Is the adjustment of the amplifier's controls and switching correct?

The specified portion of the disc cannot be played back correctly.

• Is the disc dirty or scratched?

There is a button that doesn't function.

• Has a preset such as PLAY-LOCK been set?

- · After playing a disc, always unload it from the
- · Always store the disc in the jewel case to
- high humidity.
- 3) Areas affected by heat from indoor heaters,

CD Player Type

Disc Type Standard Compact Discs (12cm, 8cm/5", 3")

AUDIO SECTION

Channels 2 Channels Stereo Sampling Frequency 44.1kHz at Normal Pitch Quantization 16-bit Linear/Channel

Oversampling 8-times, 18-bit Frequency Response 20Hz - 20kHz **Total Harmonic Distortion** 0.008% or less Signal to Noise Ratio 95dB or more Channel Separation 90dB or more Unbalanced Output RCA Jack **Output level** 2.0V at 0dB Disc Load Impedance 10k ohm or more

Balanced Output 3-Pin XLR (Active Balanced)

Output Level +18dBm at 0dB Disc (Active Balanced)

Output Adjust Range +18dBm to -20dBm or more

Load Impedance 600 ohm

Digital Output Coaxial (RCA Jack)

Headphone Output Stereo Output level 20mW or more

Load Impedance 30 to 40 ohm Variable Pitch Controll ±8% max Audio Start-up Time 0.01 second or less

Frame Search Accuracy 1/75 second

DIMENSIONS

482 (W) × 88 (H) × 266 (D) mm

19" (W) × 3-15/32" (H) × 10-15/32" (D)

excluding height of keys, connectors and foots

WEIGHT Approx. 4.5kg

POWER CONSUMPTION Approx. 14W

POWER SUPPLY AC120V ±10%, 60Hz (USA, Canada)

AC230V ± 10%, 50Hz (Europe, Others) AC115/230V ±10%, 50/60Hz (Asia, Others)

SAFETY STANDARD UL (USA) CSA (Canada)

ENVIRONMENTAL CONDITIONS

Operating Temperature

+5°C to 35°C

Humidity 25% to 85%. Non Condensing Storage Temperature

-20°C to 60°C

REMOTE

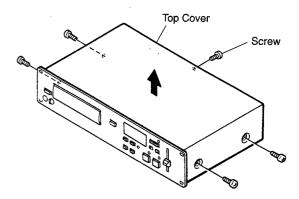
Parallel Remote, D-sub 25 pin connector

^{*} Specifications and design are subject to change without notice for purpose of improvement.

DISASSEMBLY

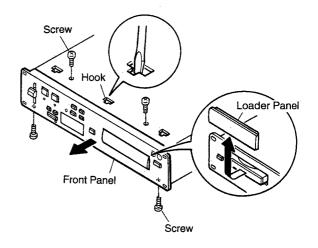
• TOP COVER

Remove 4 screws from both sides and 1 screw from Back Panel.



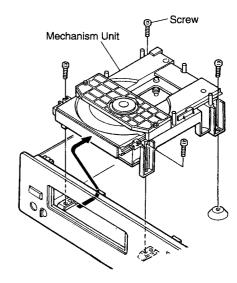
• FRONT PANEL

- 1. Pull Loader Frame frontward, and remove Loader Panel.
- 2. Remove 2 Front Panel upper screws.
- 3. Remove 2 Front Panel lower screws.
- 4. Undo 2 front panel upper hooks.
- 5. Pull Front Panel and undo a lower hook.



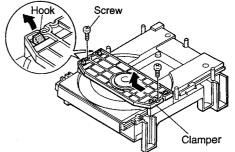
MECHANISM UNIT

Remove 4 screws.



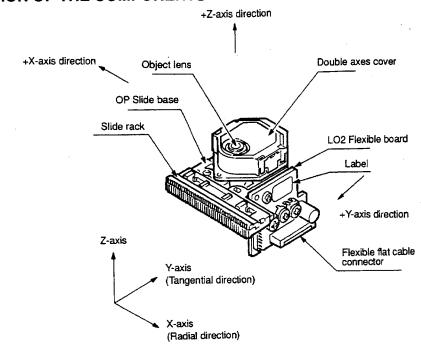
● CLAMPER

Remove 2 screws.
Pull clamper and undo 4 hooks.

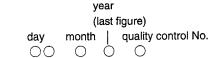


NOTE FOR HANDLING OF LASER PICK-UP

DESCRIPTION OF THE COMPONENTS



LABEL



but Oct. Nov. and Dec. are expressed by alphabetical letters of $X,\,Y$ and Z.

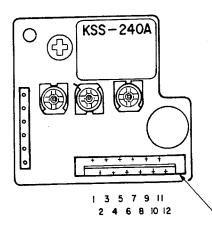


Flexible flat cable

connector

PIN CONNECTOR

The expressed unit is by mA, with omission of the decimal point as for example, 56.5mA will be expressed as 565, but the head of English letter means the control in the manufacturning plant.



Pin No.	Description	Input/ Output	Pin No.	Description	Input/ Output
1	VC (+2.5V)	OUT	7	Vcc (+5V)	IN
2	TE (TRK ER signal)	OUT	8	LDC (LD Control)	· IN
3.	FE (FCS ER signal)	OUT	9	FCS+ (Double axes)	IN
4	FZC (FZC signal)	OUT	10	TRK+ (Double axes)	IN
5	RF (RF signal)	OUT	11	TRK- (Double axes)	IN
6	GND	1N	12	FCS- (Double axes)	IN

Caution for Handling the Laser Pick-up

The laser pick-up KSS-240A is assembled and precisely adjusted using a sophisticated manufacturing process in our plant. Do not disassemble or attempt to readjust it. Please keep the following instructions carefully in handling pick-up.

1. Handle with Care

(1) Storage

Do not store the pick-up in dusty, high-temperatured or highhumidity environments.

(2) Please take care for preventing from shock by falling down or careless handling.

2. Laser Diode (LD)

(1) Protect your eyes

The laser beam may damage the human eye, since the intensity of the focused spot may reach $7\times10^3\,\text{W/cm}^2$ even if the intensity at the objective lens is 400 μW maximum. As the light beam spreads after focused through the objective lens, it does not effect you in the place as far as more than 30 cms. However, do not look at the laser light beam either through the objective lens directly nor another lens or a mirror.

(2) Poison of As

Since the LD chip contains As (Arsenic), as GaAs + GaAlAs, as known as the poison, although the poison is relatively weak, in comparing with others, e.g.As2O3, AsCl3 etc., and the amount is small, avoid putting the chip in acid or an alkali solution, heating it over 200°C or putting it into your mouth.

(3) Avoid surge current or electrostatic discharge

The LD may be damaged or deteriorated by its own strong light if a large current is supplied to it, even if only a short pulse.

Make sure that there is no surge current in the LD driving circuit by switches or else. Be careful to handle pick-up as it may be damaged in a moment by human electrostatic discharge. The pins of the LD are short-circuited by solder for protection during shipment.

For safety handling of an LD, grounding the human body, measuring equipments and jig is strongly recommended. And still it is further desirable to make use of mat on the platform and floor for handling the LD.

To open the short-circuit, remove the soldering quickly with a soldering iron whose metal part is grounded.

The temperature of the soldering iron should be less than 320°C (30W).

3. Actuator

(1) The performance of the actuator may be effected if magnetic material is located nearby, since the actuator has a strong magnetic circuit. Do not permit dust to enter through the clearance of the cover.

(2) Cleaning the lens

It may change the specifications by attaching dust or ash on the objective lens. Clean the lens with a cleaning paper dampened with a little water, not pressing lens with so much strength by the cleaning paper.

4. Metal Bearing

As the metal bearing of Cu-compound sintered alloy is impregnated with FROIL946P (*Part No. 529 0054 007), never fail to supply the bushing with the same lubricant at the time of replacing the pick-up.

5. Handling

Please handle the laser pick-up with holding the side base (rosin molded part).

When either a part of human body or some other things may happen to touch directly with the circuit part of P.W.Board, it may cause deterioration, take careful attention in handling this base.

6. Deterioration

As KSS-240 comprises built-in RF Amp and APC curcuit, it resists stronger against external electrostatic damages than the former typed pickup. However, there is possibility of pickup deterioration in the following cases.

- (1) Low HF level, or with great numbers of jitters.
- (2) Tracking offset (EF Balance) is out of order (Refer to "Confirmation Method of Adjustment" for confirmation on (1) and (2)).

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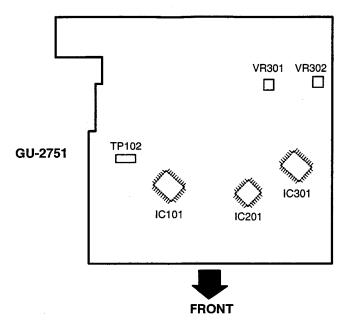
DN-650F

SERVO ADJUSTMENT

NECESSARY EQUIPMENTS FOR ADJUSTMENT

- 1. Dual trace oscilloscope
- 2. Reference disc CA1094
- 3. Frequency Counter
- 4. Filter for measurement

LOCATION



ADJUSTMENT PROCEDURE

Be sure to perform servo confirmations by this order.

- 1 Actuating the Service Program.
- 2 Confirmation of Tracking Offset.
- 3 Confirmation of HF Waveform.

1. ACTUATING THE SERVO PROGRAM

- ① Turn the power off.
- While simultaneously pushing the SEARCH buttons () and the TRACK button (), turn the power on.
- ③ Displayed indication is version number of microcomputer program 4 figures.



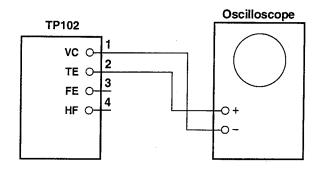


Program Version

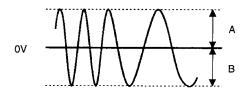
- ④ Push the TRACK button of the mechanism intended to confirm for one time. After confirm that v is displayed, push the PLAY button. Then, the Tray will open, set the disc.
- ⑤ Push the PLAY button. Then, the Tray will close.
- Push the TRACK button (#2 is indicated), then push the
 PLAY button.
- Push the TRACK button (33 is indicated), then push the PLAY button.

2. CONFIRMATION OF TRACKING OFFSET

① Connections



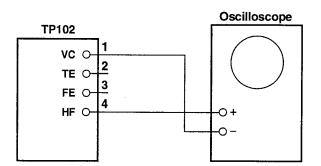
- ② Push the TRACK button ("Y is indicated), then push the PLAY button.
- 3 Observe TE on the scope.



Measure the voltage of A,B and in case $\frac{|A-B|}{A+B}$ exceeds 15%, please replace pick-up as it is defected.

3. CONFIRMATION OF HF WAVEFORM

① Connections



- ② Push the TRACK [] button ([] 5 is indicated), then push the PLAY button.
- 3 Observe HF waveform on the scope.
- ④ The standard amplitude of HF waveform is 1.1V. If it is less than 0.8V, please replace pick-up as it is defected.

4. ADJUSTMENT OF SUPER LINEAR CONVERTER

Adjustment of Super Linear Converter is only performed at a time the DA Converter is replaced.

Adjustment Procedure

① Connections

Connect the LINE OUT to a distortion meter through the low-pass filter.

- 2 Playback a disc obtains 1kHz, 0dB sine wave tone.
- 3 Adjust the RV301, RV302 and obtain minimum THD.

RV301.....L-channel RV302.....R-channel

THD standard is less than 0.006%

ABOUT THE SERVICE PROGRAM

The service program is a program specially for servo confirmations.

ACTUATING THE SERVICE PROGRAM.

① Turn the power off.

- ② While simultaneously pushing the SEARCH buttons () and the TRACK button (), turn the power on.
- 3 Program version of microcomputer indicated on the remote control signifies start actuating of service program.

CONTENTS OF SERVICE PROGRAM

After actuating the service program, select an aiming process number with the TRACK ([] buttons, END MON button, and PITCH button, and push the PLAY button to execute processing, The process number is then displayed on the TRACK indication portion.

TRACK BUTTONS	Process No. (TRACK Indication)	Function	Contents Explanation
	01	OPEN/CLOSE	Performs OPEN/CLOSE each time the PLAY button is pushed.
TRACK	02	FOCUS ERROR	Confirm FOCUS Error signal (S curve).
BUTTONS	03	FOCUS SERVO ON	Turns the FOCUS Servo ON.
H4 PH	04	Confirmation of TRACKING OFFSET	Rotates the disc. Checks divergence of Tracking Offset.
	05	Confirmation of HF	Normally the same as PLAY MODE.
	06	Cleaning of Pick-up Lens	Pick-up. moves when SEARCH () button is pressed. Move the pick-up under the hole of mechanism PWB, and clean the lens.
END MON	0A	CHUCKING Test	Repeats OPEN/CLOSE of tray, servo ON, and TOC read.
PITCH	Od	Heat Run	Repeats, OPEN/CLOSE of tray, repeats playing the first and the last programs of music on the disc. When an error occurs, displays error code and stops. (See the table below.)

Table of Error Code

Error Code	Contents
E0	Automatic adjustment of servo does not finish.
E1	Focus servo error. E1-00 No FOK is appeared. E1-01 FOK is appeared, but no FZC is shown. E1-02 Both FOK, FZC are appeared, but FZC is Shorter than mask time. E1-03 Both FOK, FZC are appeared, but FZC is not turned to "L" within prescribed time.
E2	Unable to detect sync pattern (GFS) however, rotating the disc. E2-00 FOK is turned to "L" after spindle kick. E2-01 GFS is not appeared.

E3	Unable to detect sync pattern (GFS). E3-00 In playing E3-01 in searching.
E4	Unable to read TOC when servo is actuated. E4-00 unable to read subcode. E4-02 Unable to read TOC within 15 seconds after finish reading subcode.
E5	Disc holder malfunction.
E6	Pick-up innermost circle switch does not turn OFF.
E7	Pick-up innermost circle switch does not turn ON.

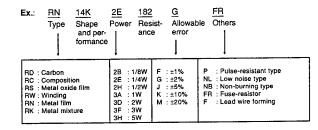
NOTE FOR PARTS LIST

NOTE FOR PARTS LIST

- Part indicated with the mark " " are not always in stock and possibly to take a long period of time for supplying, or in some case supplying of part may be refused.
- When ordering of part, clearly indicate "1" and "I" (i) to avoid mis-supplying.
- Ordering part without stating its part number can not be supplied.
- Part indicated with the mark "★" is not illustrated in the exploded view.
- Not including Carbon Film ±5%, 1/4W Type in the P.W.Board parts list. (Refer to the Schematic Diagram for those parts.) **WARNING:**

Parts marked with this symbol 🛕 📖 have critical characteristics. Use ONLY replacement parts recommended by the manufacturer.

Resistors



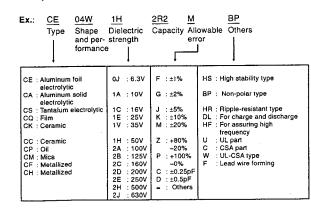
* Resistance

1 8 2 ⇒ 1800 ohm = 1.8 kohm
Indicates number of zeros after effective number.
2-digit effective number.

1 R 2 ⇒ 1.2 ohm 1-digit effective number. 2-digit effective number, decimal point indicated by R.

Units: ohr

Capacitors



* Capacity (electrolyte only)

2 2 2 ⇒ 2200µF
Indicates number of zeros after effective number.

• Units: µF.

2 R 2 ⇒ 2.2µF
1-digit effective number.
2-digit effective number, decimal point indicated by R.
Units: µF.

* Capacity (except electrolyte) 2 2 2 ⇒ 2200pF = 0.0022μF

2 2 ⇒ 2200pF = 0.0022µF

(More than 2)— Indicates number of zeros after effective number.

• Units: µF.

 When the dielectric strength is indicated in AC, "AC" is included after the dieelectric strength value.

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DN-650F

PRINTED WIRING BOARD PARTS LIST

GU-2751 MAIN P.W.B. UNIT

Ref. No.	Part No.	Part Name	Remarks
SEMICON	DUCTORS GI	ROUP	<u> </u>
IC101	262 1879 003	IC CXD2515Q	
IC102	263 0909 906	IC BA6392FPT-1	
IC103	263 0910 018	IC BA7042(20MH)	
IC104	262 1205 907	IC TC74HCU04AF	
IC105	263 0615 902	IC BA15218F	
IC106	262 2024 909	IC YSD221F	
IC201	262 2009 005	IC µPD78233GC-3B9	
IC202	262 1721 902	IC TC74HC573AF	
IC203	GEN 2842	ROM Sub Ass'y	
		(included TMS27C256-15)	
IC204	263 0652 907	IC PST529C	
IC205	262 1707 900	IC TC74HC574AF	
1C206	262 1709 908	IC TC74HC245AF	
IC207	262 0945 909	IC SN7438NS-R	
IC208 IC209	262 1718 902 262 1711 909	IC TC74HC00AF IC X24C00S	
1	262 1711 909	IC μPD6381GF	
IC301 IC302	262 1907 001	IC MSM514256B-70ZS	}
IC302	262 1765 900	IC SM5841BS	
IC303	262 1805 006	IC PCM-1700L	
IC305	263 0615 902	IC BA15218F	
IC401	263 0935 006	IC SI-3050C	
IC402	263 0800 005	IC NJM78M05FA(S)	
IC403	263 0501 003	IC NJM79M05FA	
IC501	263 0533 000	IC LC7582	
<i>i</i> I			
TR201	269 0100 907	Transistor DTA143TS(4.7K)T	Built in Resistor
TR301~304	274 0160 907	Transistor 2SD2144STPU	
TR305	269 0080 904	Transistor DTA114TS(10K)T	Built in Resistor
TR306	269 0074 903	Transistor DTC114TS(10K)	Built in Resistor
TR308	269 0100 907	Transistor DTA143TS(4.7K)T	Built in Resistor
TR501~503	269 0082 902	Transistor DTC114EKT96	Built in Resistor
TR504	269 0083 901	Transistor DTA114EKT96	Built in Resistor
D201~216	276 0432 903	Diode 1SS270ATE	
D201-210 D219	276 0432 903	Diode 1SS270ATE	
D219 D220	276 0432 903	Diode 1SS270ATE	
D301	276 0432 903	Diode 1SS270ATE	
D401	276 0597 000	Diode RBA-402	
D402	276 0405 901	Diode SIWB(A)10	
D403	276 0553 905	Diode 1SR35-200A(T93X)	
D501~504	276 0438 910	Diode MA151A	
LD502	393 9526 908	LED SLR-305VC(RED)	
LD503,504	393 9526 924	LED SLR-305MC(GRN)	
RESISTO	RS GROUP (r	ot included Carbon Fil	m ±5% 1/4W type)
VR301,302	211 6093 970	Adjust 100kohm	V06PB104
VR303	211 8006 004	Variable 2kohm	V09V25DA202
VR501	211 0763 015	Variable(Slide)	
R105	247 0009 985	Chip 10kohm, ±5% 1/10 W	RM73B103JT
R113	247 0007 945	Chip 1kohm, ±5% 1/10 W	RM73B-102JT
R114	247 0012 927	Chip 100kohm, ±5% 1/10 W	RM73B104JT
R125	247 0012 943	Chip 120kohm, ±5% 1/10 W	RM73B124JT
R126	247 0010 987	Chip 27kohm, ±5% 1/10 W	RM73B273JT
R127	247 0011 915	Chip 36kohm, ±5% 1/10 W	RM73B363JT
R128	247 0008 960	Chip 3.3kohm, ±5% 1/10 W	RM73B-332JT
R129	247 0007 974	Chip 1.3kohm, ±5% 1/10 W	RM73B-132JT
	247 0008 960	Chip 3.3kohm, ±5% 1/10 W	RM73B332JT
H3385	247 0005 905	Chip 100ohm, ±5% 1/10 W	RM73B101JT
R135 R136			1
R136	l	Chin 11kohm, +5% 1/10 W	RM73B113.IT
R136 R138	247 0009 998	Chip 11kohm, ±5% 1/10 W Chip 1kohm, ±5% 1/10 W	RM73B113JT RM73B102JT
R136	l	Chip 11kohm, ±5% 1/10 W Chip 1kohm, ±5% 1/10 W Chip 100ohm, ±5% 1/10 W	RM73B113JT RM73B102JT RM73B101JT

7				
	Ref. No.	Part No.	Part Name	Remarks
ļ	R218	247 0009 985	Chip 10kohm, ±5% 1/10 W	RM73B-103JT
	R219-223	247 0011 944	Chip 47kohm, ±5% 1/10 W	RM73B-473JT
1	R261,262	247 0009 985	Chip 10kohm, ±5% 1/10 W	RM73B103JT
	R336,337	247 0004 948	Chip 56ohm, ±5% 1/10 W	RM73B560JT
ı	R338,339	247 0009 901	Chip 4.7kohm, ±5% 1/10 W	RM73B-472JT
ı	R371,372	247 0009 985	Chip 10kohm, ±5% 1/10 W Chip 1kohm, ±5% 1/10 W	RM73B-103JT RM73B102JT
١	R373 R374	247 0007 945 247 0018 905	Chip Oohm, ±10% 1/10 W	RM73B-0R0KT
ı	R375	247 0018 903	Chip 75kohm, ±5% 1/10 W	RM73B-750JT
١	R398.399	247 0007 945	Chip 1kohm, ±5% 1/10 W	RM73B-102JT
١	R501	247 0011 957	Chip 51kohm, ±5% 1/10 W	RM73B513JT
١	R502	247 0013 942	Chip 330kohm, ±5% 1/10 W	RM73B-334JT
١	R503,504	247 0003 965	Chip 27ohm, ±5% 1/10 W	RM73B270JT
1	R505~507	247 0005 989	Chip 220ohm, ±5% 1/10 W	RM73B-221JT
١	R508	247 0003 965	Chip 27ohm, ±5% 1/10 W	RM73B-270JT
	CAPACITO	ORS GROUP	L,	
	C101	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
Ì	C102	254 4252 930	Electrolytic 100µF/10V	CE04W1A101MT
1	C106	254 4254 941	Electrolytic 100µF/16V	CE04W1C101MT
Į	C107~109	253 4538 949	Ceramic 100pF/50V	CC45SL1H101JT
	C110	254 4260 964	Electrolytic 3.3µF/50V	CE04W13HR3MT
ı	C111	254 4254 909	Electrolytic 10µF/16V	CE04W1C100MT
1	C112	253 9031 904	Ceramic 0.047µF/25V	CK45=1E473KT
i	C113	253 4536 970	Ceramic 20pF/50V	CC45SL1H200JT
١	C114	257 0014 935	Ceramic(Chip) 0.1µF/25V	CK73F1E104ZT
ı	C115	254 4252 930	Electrolytic 100μF/10V	CE04W1A101MT
ı	C116 C117	257 0010 900	Ceramic(Chip) 0.01µF/50V	CK73B1H103KT CC73SL1H100DT
	C117	257 0002 921 253 9030 963	Ceramic(Chip) 10pF/50V Ceramic 0.01µF/25V	CK45=1E103KT
١	C119	253 4538 949	Ceramic 100pF/50V	CC45SL1H101JT
١	C121	253 9030 963	Ceramic 0.01µF/25V	CK45=1E103KT
	C122,123	253 4444 907	Ceramic 220pF/50V	CC45SL1H221JT
ı	C124	253 4456 908	Ceramic 680pF/50V	CC45SL1H681JT
١	C125	257 1013 964	Ceramic(Chip) 0.056µF/25V	CK73B1E563KT
١	C126	257 0007 942	Ceramic(Chip) 0.0015µF/50V	CC73SL1H152JT
ł	C127	253 4444 907	Ceramic 220pF/50V	CC45SL1H221JT
ı	C128	253 9030 963	Ceramic 0.01µF/25V	CK45=1E103KT
۱	C130,131 C132	253 9030 963 257 0001 951	Ceramic 0.01µF/25V Ceramic(Chip) 3pF/50V	CK45=1E103KT CC73SL1H3R0CT
ı	C132	257 0001 931	Ceramic(Chip) 5pF/50V	CC73SL1H5R0CT
١	C135	257 0010 900	Ceramic(Chip) 0.01µF/50V	CK73B1H103KT
١	C141	254 4258 905	Electrolytic 4.7µF/35V	CE04W1V4R7MT
1	C150	257 0014 935	Ceramic(Chip) 0.1µF/25V	CK73F1E104ZT
_1	C151	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
٦	C180	254 4254 925	Electrolytic 33µF/16V	CE04W1C330MT
┨	C181	253 4538 949	Ceramic 100pF/50V	CC45SL1H101JT
1	C182	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
1	C201	254 4254 954	Electrolytic 220µF/16V	CE04W1C221MT
1	C202 C204	253 9039 906 253 9039 906	Ceramic 0.1μF/25V Ceramic 0.1μF/25V	CK45=1E104ZT CK45=1E104ZT
	C230	257 0014 935	Ceramic(Chip) 0.1µF/25V	CK73F1E104ZT
	C230	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
	C232	253 9030 992	Ceramic 0.033µF/25V	CK45=1E333KT
1	C233	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
١	G280	257 0014 935	Ceramic(Chip) 0.1µF/25V	CK73F1E104ZT
1	C303~305	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
	C307,308	253 4536 983	Ceramic 22pF/50V	CC45SL1H220JT
	C309,310	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
1	C311	254 4254 941	Electrolytic 100μF/16V	CE04W1C101MT
ĺ	C313	254 4252 930	Electrolytic 100µF/10V	CE04W1A101MT
	C314	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
	C316	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
	C317	254 4252 930	Electrolytic 100µF/10V	CE04W1A101MT
1	C318,319	253 9030 963	Ceramic 0.01µF/25V	CK45=1E103KT

GU-2752 OUTPUT P.W.B. UNIT

				GU-2/52 C	א וטאוטי.	W.B. UNII	
Ref. No.	Part No.	Part Name	Remarks	Ref. No.	Part No.	Part Name	Remarks
C320	253 4456 908	Ceramic 680pF/50V	CC45SL1H681JT	SEMICON	DUCTORS G	ROUP	
C321	253 9030 963	Ceramic 0.01µF/25V	CK45=1E103KT	IC601,602	263 0360 008	IC NE5532	
C322	253 4456 908	Ceramic 680pF/50V	CC45SL1H681JT	10001,002	203 0300 006	IC NE3332	
C323~327	253 9030 963	Ceramic 0.01µF/25V	CK45=1E103KT	D601,602	276 0432 903	Diode 1SS270A TE	
C328,329	253 4456 908	Ceramic 680pF/50V	CC45SL1H681JT	1 5007,002	2100402300	Dioce 1002/07/12	
C330,331	255 1265 907	Film 0.0068µF/50V	CQ93M1H682JT B		<u> </u>		
C332,333	254 4252 930	Electrolytic 100μF/10V	CE04W1A101MT	RESISTOR	RS GROUP (r	not included Carbon File	n ±5% 1/4W type)
C334,335	253 4537 982	Ceramic 56pF/50V	CC45SL1H560JT	VR601,602	211 0552 006	Variable 1k ohm	V09QA102
C336,337	254 4252 930	Electrolytic 100μF/10V	CE04W1A101MT				
C338,339	257 0007 900	Ceramic(Chip) 0.001µF/50V	CC73SL1H102JT	04040		l	<u> </u>
C340	254 4254 954	Electrolytic 220µF/16V	CE04W1C221MT	CAPACITO	RS GROUP		
C341,342	253 1180 921	Ceramic 0.001 µF/50V	CK45B1H102KT	C601,602	253 4536 970	Ceramic 20pF/50V	CC45SL1H200JT
C345	253 9039 906	Ceramic 0.1 µF/25V	CK45=1E104ZT	C603,604	254 3052 940	Electrolytic 220µF/10V(Bipolar)	CE04D1A221MBPT
C371	257 0014 935	Ceramic(Chip) 0.1µF/25V	CK73F1E104ZT	C605,606	253 4536 970	Ceramic 20pF/50V	CC45SL1H200JT
C382	254 4252 930	Electrolytic 100µF/10V	CE04W1A101MT	C607,608	254 3052 940	Electrolytic 220µF/10V(Bipolar)	CE04D1A221MBPT
C401	254 4254 912	Electrolytic 22µF/16V	CE04W1C220MT	C609~613	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT
C402	254 4255 717	Electrolytic 4700µF/16V	CE04W1C472MC			·	
C403,404	254 4255 704	Electrolytic 3300µF/16V	CE04W1C332MC	OTUED D	DTO		· · · · · · · · · · · · · · · · · · ·
C405,406	254 4254 941	Electrolytic 100µF/16V	CE04W1C101MT	OTHER PA	HIS		
C501	257 0006 969	Ceramic(Chip) 680pF/50V	CC73SL1H681JT	RL601,602	214 0179 006	Relay (EA2-5)	
C502	257 2004 901	Tantalum electrolytic 1µF/16V	CS77B1C010MT	11			
C503	257 0014 935	Ceramic(Chip) 0.1µF/25V	CK73F1E104ZT	JA601,602	205 0428 009	3P Cannon Connector	
C800~802	253 9039 906	Ceramic 0.1µF/25V	CK45=1E104ZT				
C803	257 0014 935	Ceramic(Chip) 0.1μF/25V	CK73F1E104ZT	CN601	205 0406 063	6P Connector Base(KR-PH)	
OTHER PA	ARTS			7 L 			
X101	399 0036 013	Crystal Resonator (16.9344MHz	<u>.</u>)				
X201	399 0038 901	Ceramic Resonator (CST12.0M	TW-TF1)	DDINTE	D WIRI	NG BOARD	
X301	399 0141 005	Ceramic Resonator (CSA24.57)	MX040)	FRINCE	D AAIIII	NG DOAND	
			1			·	
LC501	393 4139 002	LCD		GU-2752 O	UTPUT U	NIT	
LE501	393 9511 201	LED Back Light	For LC501				
RL201	214 0179 006	Relay (EA2-5)		VR66	R60L	\ O\	U-2752
SW401	212 1039 000	1P Push Switch		1 1	4 8		1c610
SW501~512	212 4775 905	Tact Switch			-MB60		T 1

GU-2767 CN P.W.B. UNIT

204 8262 002

204 8311 021

204 8264 026

205 0581 001

203 2318 014

205 0355 033

205 0406 047

205 0406 047

205 0343 058

205 0321 054

205 0535 031

205 0406 063

205 0683 006

205 0736 047

205 0702 071

231 8063 009

233 6122 003

233 6121 004

233 6120 005

205 0825 000

1P Pin Jack

2P Pin Jack

Head Phone Jack

2P VH Connector Base

4P Connector Base(KR-PH)

4P Connector Base(KR-PH)

5P Connector Base(KR-PH)

5P Connector Base(RED)

6P Connector Base(KR-PH)

12P FFC Connector Base

17P FFC Connector Base

17P FFC Connector Base (L)

U.S.A. and Canada Model

Multi-Voltage Model only

Europe Model

Multi-Voltage Model

6P Connector Base

Pulse Trans

Power Trans

Power Trans

Power Trans

3P AC Connector Base

2P SAN-SAN Cord 3P KR Connector Base L

JA101

JA302

CN2B

CN3A

CN4A

CN4B

CN5A

CN5B

CN6A

CN6B

CN12A

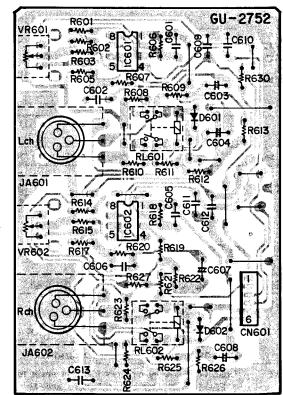
CN17A

CN17B

T201

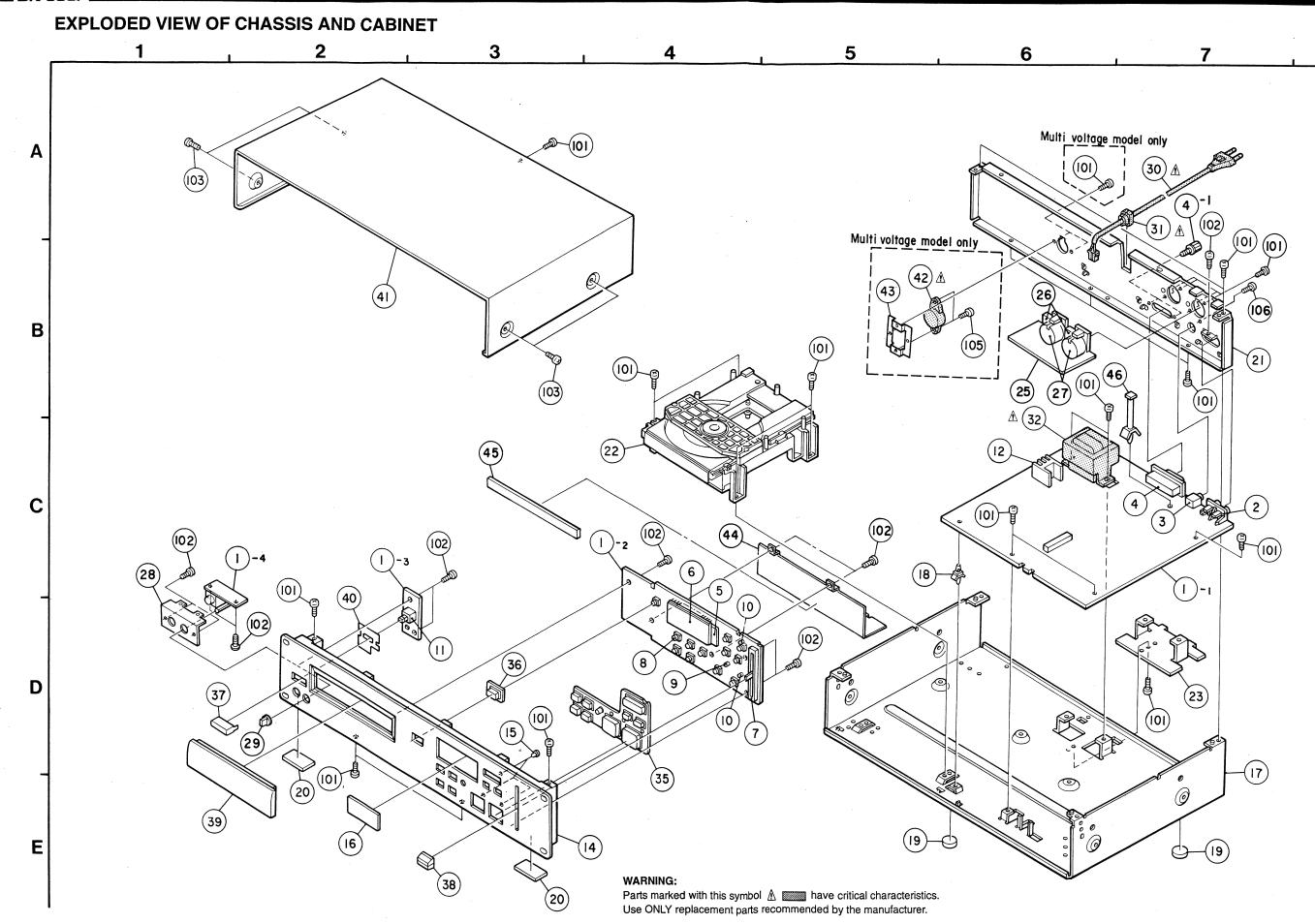
△ T401

Ref. No.	Part No.	Part Name	Remarks
CN6C	205 0536 030	6P Connector Socket	
CN6D	205 0848 906	6P FFC Connector Base	P=0.8



PRINTED WIRING BOARD PATTERNS 6 8 **GU-2751 MAIN UNIT** GU -2751- I 🎍 CN3C (Asia Only) GU-2751-3 000 perro GU-2767

GU-2751-2



PARTS LIST OF EXPLODED VIEW

		Part Name	Remarks	Q'ty	Ref. No.	Part No.	Part Name	Remarks	Q'ty
l "	GU-2751	Main PWB Unit Ass'y		1	SCREV	vs			
1		Main PWB Unit		1	101	473 7002 021	Tapping Screw 3×8(S)	Black	24
2		Panel PWB Unit		1	i	Į.			12
3		Switch PWB Unit		1	i i	1	1	Black	4
4		Head phone PWB Unit		1	1	1	_		2
2	204 8311 021	2P Pin Jack		1		i	1		1
3	204 8262 002	1P Pin Jack		1	100	4737300017	rapping colon extent		
1	205 0618 110	25P D sub socket	With Screw	1	1				
5	393 9511 201	LED Back Light		1	İ				
3	393 4139 002	LCD		1					
7	211 0763 015	Slide Volume		1					
3		Tact Switch (Long ST)		12					
			SLR-305VC	1	1				j
0				2					
1	1	· · ·		1	ŀ				
- 1	1			1	1				
- 1									
	l l								
- 1				i 1					
6		1							
7	411 0962 801	Chassis		1					
8	443 0518 003			1					
9	461 0706 114	Foot Sheet		1					
0	461 0740 015	Sheet		2					
1	105 1130 106	Back Panel	U.S.A. and Canada Model	1					
	105 1130 119	Back Panel	Europe Model	1					
		Back Panel	Multi-Voltage Model	1					
2	1		•	1					
	1			1 1					
- 1	i			1	1				'
- 1					1				
- 1	1	•							
i				1 . 1					
									-
!9 0	112 0645 182 206 2110 004	AC Cord W/CONN.	U.S.A. and Canada	1					
	206 2089 106	AC Cord W/CONN.	Europe and Multi-Voltage	1					
			14. A Compression of the Comp		1				
	206 2128 009		U.K.Model						
11			1104 - 10 · 1						
12	233 6122 003	Power Trans		1 1		1			1
0000000000									
			Multi-Voltage Model	P ()	1				
35									1
36	119 0072 015	' '		1					
37	113 1357 265	Power SW. Button		1					
38	113 1523 015	Slide Knob		1					
39	146 1394 134	Loader Panel		1					
40	441 1627 117	P.Button Guide		1]]			,	
41	102 0425 224	Top Cover		1					
42	212 0359 008	Voltage Selector	Multi-Voltage Model only	1					
43	412 3629 102	VOL Selector Bracket	Multi-Voltage Model only	1	11				
44	l	P.W.B. Guide	-	1	11				
45	ì			1					
46		1		1					
	,-10 0000 000								
	3 4 2 3 4 5 6 7 8 9 9 0 1 2 3 5 6 7 8 9 9 0 1 1 2 3 14 15 14 15 15 16 17 8 9 9 0 1 1 2 13 14 15 15 16 17 8 9 9 10 11 2 13 14 15 16 17 8 9 10 11 2 13 14 15 15 16 17 8 9 10 11 2 13 14 15 15 16 17 8 19 10 11 2 13 14 15 15 16 17 8 19 10 11 2 13 14 15 15 16 17 8 19 10 11 2 13 14 15 15 16 17 8 19 10 11 2 13 14 15 15 16 17 8 19 10 11 2 13 14 15 15 16 17 8 19 10 11 2 13 14 15 15 16 17 8 19 10 11 2 13 14 15 15 16 17 8 19 10 11 2 13 14 15 16 17 8 19 10 11 12 13 14 15 16 17 8 19 10 11 12 13 14 15 16 17 8 19 10 11 12 13 14 15 16 17 8 19 10 11 12 13 14 15 16 17 8 19 10 11 12 13 14 15 16 17 8 10 10 10 10 10 10 10 10 10 10 10 10 10	3	Switch PWB Unit Head phone PWB Unit Head phone PWB Unit PPin Jack 1	Switch PWB Unit Head phone PWB Unit Head phone PWB Unit 2P Pin Jack 20 05 0618 110 2P Pin Jack 393 9511 201 LED Back Light LCD 211 0763 015 212 4775 905 212 4775 905 212 4775 905 212 4775 905 212 4775 905 212 1038 900 211 0763 015 212 1038 900 21 17 0462 118 212 1039 900 21 17 0462 118 214 17 0462 118 214 1371 105 214 1371 905 214 17 0462 118 215 146 1371 905 216 146 1371 905 216 146 1496 929 217 411 9962 801 218 443 0518 903 219 461 0764 114 219 105 1130 116 210 1130 116 211 1055 206 211 0552 906 211 0552 906 211 0552 906 212 2035 908 212 048 909 206 2110 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 210 004 206 218 909 206 218 909 206 218 909 207 218 909 208 2089 106 208 2089 106 208 2089 106 208 2089 106 208 218 909	Switch PWB Unit Head phone PWB Unit	Switch PWB Unit Head phone PWB Unit	Switch PWB Unit	Switch PWB Unit Head phone PWB Unit 1 1 105 105 116 116 115 117 205 116 116 115 117 205 116 116 117 205 116 116 117 205 116 116 117 205 116 116 116 116 116 116 116 116 116 11	Salch PWB Unit Head phone PWB Unit 2P Pin Jack 1 105 27 7005 002 Tapping Grow 3-010(S) 2P Jack 2P Pin Jack 1 106 2P Pin Jack 2P Pin Jack 1 106 2P Pin Jack 2P Pin

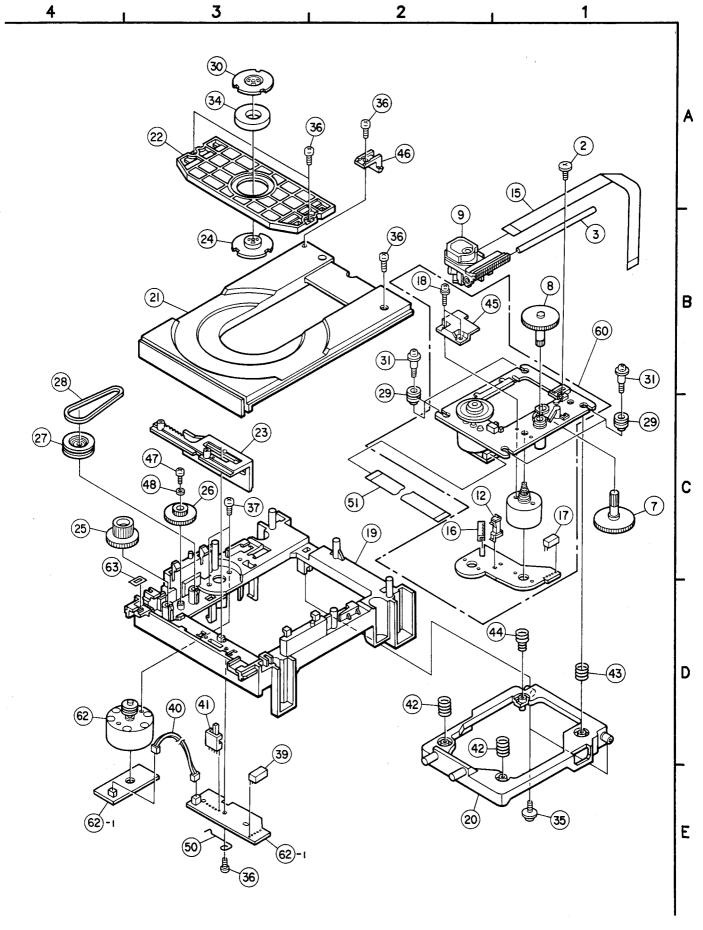
Part indicated with the mark * ® " are not always in stock and possibly to take
a long period of time for supplying, or in some case supplying of part may be
refused.

PACKING & ACCESSORIES

PARTS LIST OF FG-71 MECHANISM UNIT

Ref. No.	Part No.	Part Name	Remarks	Q'ty	Ref. No.	Part No.	Part Name	Remarks
	511 2647 000	INST.Manual		1	2	9KA 90H0 06	FS Fixing Screw	
	511 2648 009	INST.Manual	Europe Model Only	1	3	9KA 90H0 05	Feed Shaft	
	515 0692 004	DEL Warranty COM.	U.S.A. and Canada	1	7	9KA 80G0 17	Drive Gear (A)	
			Model Only	1 1	8	9KA 80G0 18	Drive Gear (B)	
	203 2360 004	2P Pin Cord		1	9	499 0191 009	Laser P.U	KSS-240A
	504 0092 060	Styrene Paper	For AC Cord	1	12	9KS 01W1 47	Leaf Switch	
1	505 0038 030	Poly Cover		1	13		_	
	202 0042 004	AC Adapter	Multi-Voltage Model	1	15	009 0051 001	12P FFC Cable	
			Only		16	443 1093 006	FFC Bush	
	503 1130 009	Cushion		2	17	9KA 82G2 53	S5B-PH Connector Base	
	501 1739 132	Carton Case		1	18	9KM 20S0 04	2x4 Screw	
	505 0102 092	Styrene Paper			19	9KA 85G0 26	MECHA.Plate(FG70)	
					20	9KA 85G0 20	MECHA.Frame(FG70)	
					21	9KA 85G0 21	CD Tray(FG70)	
			:		22	9KA 85G0 04	Clamper Frame	
					23	9KA 85G0 22	UD Plate Gear(FG70)	
					24	9KA 85G0 06	Clamper (F)	
					25	9KA 85G0 07	Relay Gear(A)	
1		+			26	9KA 85G0 08	Relay Gear(B)	
					27	9KA 85G0 09	Relay Gear(C)	
[28	9KA 85G0 10	Gear Belt(F)	
					29	9KA 85G0 30	Damper(FG40)	
					30	9KA 85P0 01	Clamper Plate (F)	
					31	9KA 85H0 01	Screw(F)	
					34	9KA 82G0 57	Magnet	
1					35	9KA 91H0 02	3x8 (W-10) Screw	
					36	9KB 30B0 08	3x8 Baind Screw	
					37	9KM 26B0 04	2.6x4 Baind Screw	
					39	9KA 82G3 08	S5B-PH(RED)	
					40	9KA 85G0 27	CNW2(FG70)	
					41	9KS 01W1 48	OP/CL Switch(SSS12)	
					42 43	9KA 85S0 01 9KA 85S0 02	Spring (A)	
					44	9KA 85S0 02	Spring (B) Spring (C)	
					44	9KA 85G0 33	Gear Guide	
					46	9KA 85G0 36	Tray Stopper	
					47	9KB 20B0 05	2x5 Baind (B)	
					48	9KS 21W6 04	STW 2.1x6x0.4	
					50	9KA 85S0 05	Hold Spring	
		•			60	3104 0300 03	Spindle Motor Ass'y	
					61		Opiniolo Motor Ass y	
					62	9KA 85A0 06	Loading Motor Ass'y	
					62-1	9KA 85P0 05	Motor P.W.B.	
					63	9KA 85P0 18	Spacer 72	
					"	2.2.30.010		
]
	,							
							•	
			*					
								

EXPLODED VIEW OF FG-71 MECHANISM UNIT

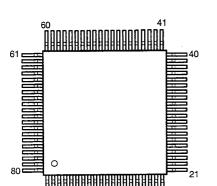


WIRING DIAGRAM 1 3 2 LINE OUT A DIGITAL OUT CN25 CN17A В CN4A C CN 4 13/2 MULTI-VOLTAGE MODEL ONLY VOLTAGE SELECTOR CN 4 2 2 -CN3C CN5A D KSS240A OPTICAL PICK UP SPINDLE MOTOR LOADER MOTOR OPEN/CLOSE SW O SLIDE MOTOR E

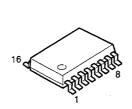
SEMICONDUCTORS

● IC's

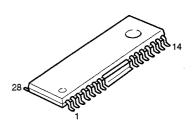
μPD78233GC-3B9(IC201)



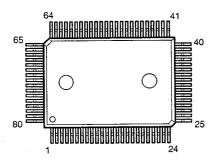
YSD221F (IC106)



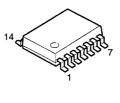
BA6392FP-T1(IC102)



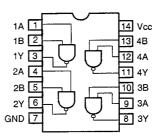
μPD6381GF(IC301)



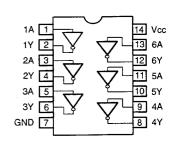
SN7438NS (IC207) HD74HC00FP (IC208) TC74HCU04FP (IC104)



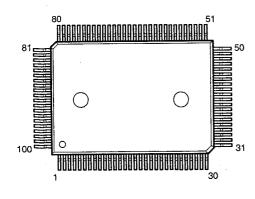
HD74HC00FP SN7438NS



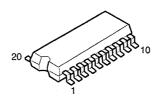
TC74HCU04FP



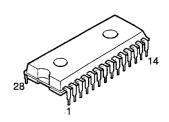
CXD2515Q(IC101)



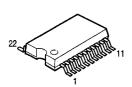
TC74HC573AF(IC202) TC74HC574AF(IC205) HD74HC245FP(IC206)



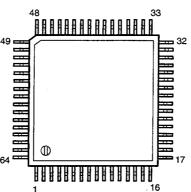
TMS27C256-15(IC203) PCM-1700L(IC304)



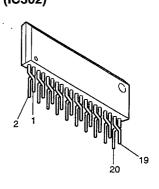
SM5841BS(IC303)



LC7582(IC501)



MSM514256B-70ZS (IC302)



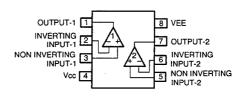
BA15218F(IC105,305) X24C00 (IC209)

BA7042(IC103) NE5532 (IC601,602)

BA15218F NE5532





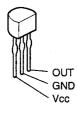


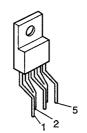
PST529C(IC204)

SI3050C (IC401)

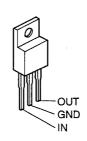
NJM78M05FA (IC402)

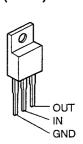
NJM79M05FA (IC403)







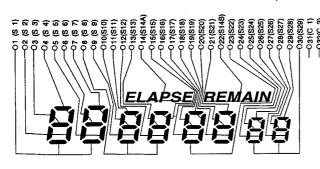


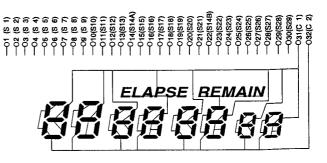


LCD(LC501)

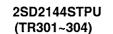
Segment

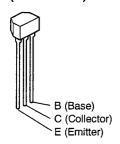
Common



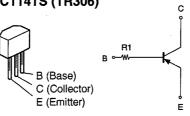


TRANSISTORS

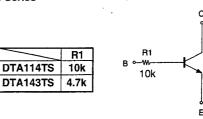




DTA114TS (TR305) DTA143TS (TR201) DTC114TS (TR306)



DTA TS Series

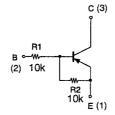


DTC 114 TS

DTA114EK (TR504)



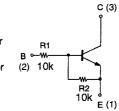
- 1: GND/Emitter
- 2: In/Base
- 3: Out/Collector



DTC114EK (TR501~503)



- 1: GND/Emitter
- 2: In/Base 3: Out/Collector

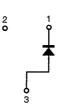


DIODES

MA151A(D501~503)

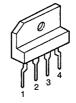


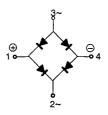
- 1: Cathode
- 2: NC 3: Anode



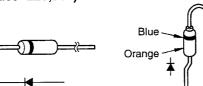
1SR35-200A (D403)

RBA402(D401)



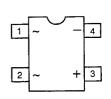


1SS270ATE (D203~223,301)



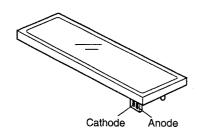
S1WB(A)10(D402)



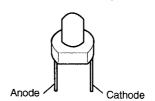


LED

BACKLIGHT(LE501)



SLR-305VC(RED)(LE502) SLR-305MC(GRN)(LE503,504)



IC TERMINAL FUNCTION LIST

TABLE OF MICROCOMPUTER μ PD78233GC-3B9 (IC201) TERMINALS

Terminal No.	Symbol Name	I/O	Terminal Function
1	SCL	0	Serial communication enable signal for connected RC-35. Data read/write clock for X24C00 (IC209).
2	RST2	0	Reset signal of IC301 (µPD6381GF).
3	LED0	0	LED/KEY scan matrix signal 1.
4	LED1	0	LED/KEY scan matrix signal 2.
5	LED2	0	LED/KEY scan matrix signal 3.
6	SDA	1/0	Communication reserved signal or busy signal for CD1. Read/write data of IC209.
7	RST-	I	Hard reset input. Reset at "L".
8	V_{DD}	_	+5V power supply.
9	X2	ı	Clock oscillation circuit input 2.
10	X1	l	Clock oscillation circuit input 1.
11	V _{SS}		0V power supply.
12	CLCK	0	Clock for servo command, level command. Connected to IC101 (CXD2515), 303 (SM5841BS).
13	DATA	0	Data for servo command, level command. Connected to IC101 (CXD2515), 303 (SM5841BS).
14	XLAT	0	Latch pulse of servo command. Latched at falling edge.
15	SCLK		Clock for data reading from IC101 (CXD2515).
16	LDON	0	Laser ON/OFF signal of optical pickup. Lase emits light at "H".
17	LCLK	0	Command transmitting clock for LCD driver.
18	LDAT	0	Command data for LCD driver.
19	LCE	0	Chip enable signal for LCD driver.
20	SRST-	0	Reset signal of IC101 (CXD2515).
21		0	Not Used.
22	RE-	0	Enable signal output for external ROM. Mask item fixed to "L", external ROM pulse output for writing.
23	OE-	0	Enable signal output for external ROM. Mask item fixed to "L", external ROM pulse output for reading.
24	CS-	0	Chip select signal of IC301. Normally "H". "L" at select only.
25	C-/D	0	Command data designate signal of IC301. Command at "L", indicates data transmitting mode at "H".
26	SCK-	0	Clock for command transmission to IC301.
27	SI	0	Command data to IC301.
28	A15	0	Memory address 15. Not used. Mask item fixed to "L".
29	A14	0	Memory address 14. Mask item fixed to "L".
30	A13	0	Memory address 13. Mask item fixed to "L".
31	A12	0	Memory address 12. Mask item fixed to "L".
32	A11	0	Memory address 11. Mask item fixed to "L".
33	A10	0	Memory address 10. Mask item fixed to "L".
34	A9	0	Memory address 9. Mask item fixed to "L".
35	A8	0	Memory address 8. Mask item fixed to "L".
36	AD7	1/0	Data bus 7. Mask item fixed to "L".
37	AD6	1/0	Data bus 6. Mask item fixed to "L".
38	AD5	1/0	Data bus 5. Mask item fixed to "L".
39	AD4	1/0	Data bus 4. Mask item fixed to "L".
40	AD3	1/0	Data bus 3. Mask item fixed to "L".
41	AD2	1/0	Data bus 2. Mask item fixed to "L".
42	AD1	1/0	Data bus 1. Mask item fixed to "L".
43	AD0	1/0	Data bus 0. Mask item fixed to "L".
44	ASTB	0	Pulse for address latch. Mask item fixed to "L".
45	V _{SS}		0V power supply.
46	MODE	I	Memory mode selection terminal. Use external ROM at "H", use mask ROM at "L". Mask item "L", external ROM "H".
47	AMUTE	0	Audio output mute signal. Mute at "H".
48	SQCK	0	Clock for sub-code reading.

Terminal No.	Symbol Name	I/O	Terminal Function
49	SENS	1	Indication signal of servo actuating condition. Emits from IC101.
50	CLOSE-	1	Tray CLOSE switch. CLOSE state at "L".
51	OPEN	ı	Tray OPEN switch. OPEN state at "L".
52	sqso	ı	Sub-code data input. Emits from IC101.
53	DFLAT	0	Command latch pulse for digital filter. Ouptut to IC303.
54	CLD	0	Serial communication enable signal for connected DN-1000F.
55	V _{DD}		+5V power supply.
56	CDNO	0	Machanism number input. Mechanism 1 at "L". Mechanism 2 at "H".
57	_	ı	Not used. Fixed to "L".
58	_	ı	Not used. Fixed to "L".
59	PITCH	ı	Pitch volume input.
60	PMODE	1	Mode input for player.
61	so	1	Serial communication input to IC301. (Normally "H")
62	_	1	Not used. Fixed to "L".
63	FOK	ı	Input terminal.
64	AV _{DD}		+5V power supply for A/D converter.
65	AVREF1		+5V. A/D converter reference voltage.
66	AVss	_	0V power supply for A/D converter.
67	LOADER	0	Tray drive signal. Stops at 2.5V. CLOSE action at 3V. OPEN action at 2V.
68	_	0	Not used.
69	AVREF2	_	+5V. D/A converter reference voltage.
70	AVREF3	_	0V. D/A converter reference voltage.
71	KIN0	ı	Key data 0.
72	KIN1	I	Key data 1.
73	KIN2	I	Key data 2.
74	KIN3	l	Key data 3.
75	KIN4	ı	Key data 4.
76	RST	ı	Input for +5V voltage observation. Shifts to "H" when POWER switch is turned off.
77	SCOR	1	Sub code sink input. Connect to IC101. Input 75 pulses per 1 second.
78	REMOT	1	Infrared-ray remote control signal input.
79	RXD-	1	Serial interface reception data.
80	TXD~	0	Serial interface transmission data.

TABLE OF DIGITAL SIGNAL PROCESSOR μPD6381GF (IC301) TERMINALS

Terminal No.	Symbol Name	1/0	Terminal Function
1	DRDY	0	Command reception READY signal from microcomputer. Normally "H".
2	FSMASK	1	LRCK mask signal. Fixed to "L".
3	SEL	1	Clock input select. Fixed to "H".
4	_	ı	Not used.
5	хо	0	X'tal oscillation output.
6	ΧI	ı	X'tal oscillation input.
7	GND	_	0V power supply.
8	XFSO	0	Clock Output. Not used.
9	-		Not connected.
10	LRCKO	0	LR clock output. 44.1kHz.
11	WCLKO	0	Word clock output. 88.2kHz. Not used.
12	BCLKO	0	Bit clock output. 2.1MHz.
13	BRAK-	0	Break acknowledge output. Fixed to "H".
14	GND	_	0V power supply.
15	BRRQ-	ı	Break request input. Fixed to "H".
16	FSRST-	1	Program counter reset input. Fixed to "H".
17	RST2-	ı	Soft reset input. Normally "H".
18	RST-	ı	Hard reset input. Normally "H".
19	A0	0	External RAM address 0.
20	A1	0	External RAM address 1.
21	A2	0	External RAM address 2.
22	A3	0	External RAM address 3.
23	A4	0	External RAM address 4.
24	A5	0	External RAM address 5.
25	A6	0	External RAM address 6.
26	A7	0	External RAM address 7.
27	A8	0	External RAM address 8.
28	A9	0	External RAM address 9. Not used.
29	A10	0	External RAM address 10. Not used.
30	A11	0	External RAM address 11. Not used.
31	A12	0	External RAM address 12. Not used.
32	A13	0	External RAM address 13. Not used.
33	V _{DD}	_	+5V power supply.
34	A14	0	External RAM address 14. Not used.
35	A15	0	External RAM address 15. Not used.
36	A16	0	External RAM address 16. Not used.
37	RAS-	0	External RAM low address strobe signal.
38	CAS-	0	External RAM column address strobe signal.
39	WE-	0	External RAM write enable signal.
40	I01	1/0	External RAM data 1.
41	102	1/0	External RAM data 2.
42	103	1/0	External RAM data 3.
43	104	1/0	External RAM data 4.
44	105	1/0	External RAM data 5. Not used.
45	106	1/0	External RAM data 6. Not used.
46	107	1/0	External RAM data 7. Not used.
47	108	VO	External RAM data 8. Not used.
48	109	I/O	External RAM data 9. Not used.

Terminal No.	Symbol Name	1/0	Terminal Function
49	1010	1/0	External RAM data 10. Not used.
50	1011	1/0	External RAM data 11. Not used.
51	1012	1/0	External RAM data 12. Not used.
52	1013	1/0	External RAM data 13. Not used.
53	1014	1/0	External RAM data 14. Not used.
54	1015	1/0	External RAM data 15. Not used.
55	1016	1/0	External RAM data 16. Not used.
56	GND		0V power supply.
57	MD0	1	Mode select 0. Fixed to "L".
58	MD1	ı	Mode select 1. Fixed to "H".
59	MD2	1	Mode select 2. Fixed to "L".
60	BCLK1	ı	Bit clock input. 2.18MHz.
61	LRCK1	i	LR clock input. 44.1kHz.
62	BCLK2	t	Fixed to "L". Not used.
63	LRCK2	ı	Fixed to "L". NOt used.
64	DI1	Ī	Data input.
65	DO1	0	Data output.
66	D12	ı	Fixed to "L". Not used.
67	DO2	0	Not used.
68	DO3	0	Not used.
69	DORQ-	l l	Not used. Fixed to "H".
70	GF-	0	G flag output. Normally "H".
71	OVF-	0	Over flag output. Normally "H".
72	V _{DD}		+5V power supply.
73	TEST0	ı	Fixed to "H".
74	TEST1	ı	Fixed to "H".
75	SETRDY	0	Not used.
76	so	0	Serial data output.
77	SCK-	ı	Serial data input/output clock.
78	SI	ı	Serial data input.
79	C-/D	ı	Command /data designation signal. "L" - command, "H" - data.
80	CS-	ı	Chip select input.

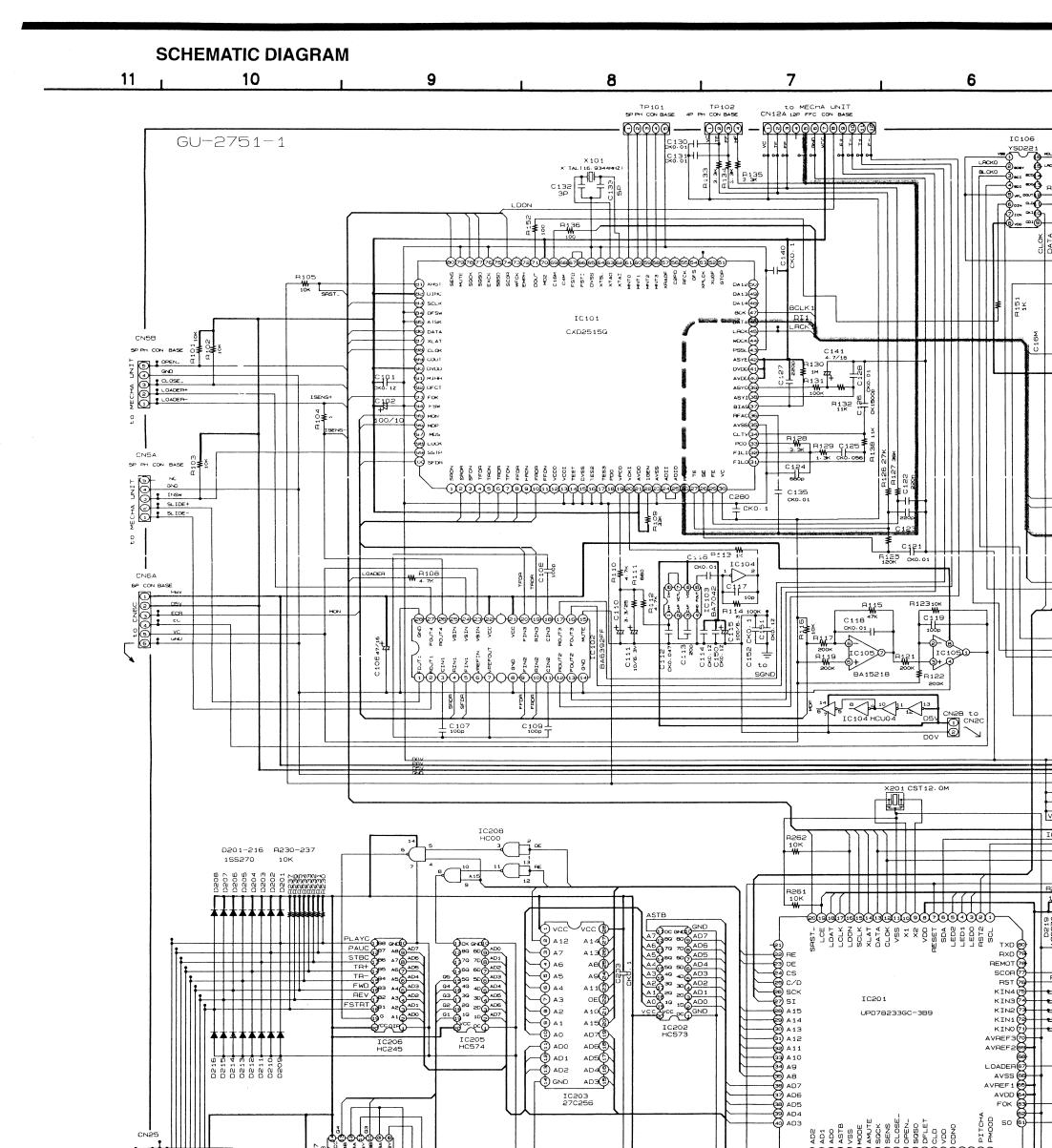
CXD2515Q (IC101) TERMINAL FUNCTION

Términal No.	Symbol Name	I/O	Terminal Function
1	SRON	0	Sled drive output.
2	SRDR	0	Sled drive output.
3	SFON	0	Sled drive output.
4	TFDR	0	Tracking drive output.
5	TRON	0	Tracking drive output.
6	TRDR	0	Tracking drive output.
7	TFON	0	Tracking drive output.
8	FFDR	0	Focus drive output.
9	FRON	0	Focus drive output.
10	FRDR	0	Focus drive output.
11	FFON	0	Focus drive output.
12	VCOO	0	Oscillation circuit output for analog EFM PLL.
13	VCOI	1	Oscillation circuit input for analog EFM PLL. fLOCK≃8.6436MHz.
14	TEST	ı	Test terminal, normally GND.
15	Vss	_	Digital GND.
16	TES2	l	Test terminal, normally GND.
17	TES3	I	Test terminal, normally GND.
18	PDO	0	Charge pump output for analog EFM PLL.
19	VPCO	0	PLL charge pump output for variable pitch.
20	VCKI	ı	Clock input from external VCO for variable pitch. fCENTER=16.9344MHz.
21	AV _{DD}	-	Analog power supply.
22	IGEN	ı	Current source reference resistor connecting terminal for OP amplifier.
23	AV _{SS}	_	Analog GND.
24	ADII	ı	A/D converter input terminal.
25	ADIO	0	OP amplifier output terminal.
26	RFDC	1	RF signal input. Input range 2.15–5.0V (at V _{DD} =AV _{DD} =5.0V).
27	TE	1	Tracking error signal input. Input range 2.5V±1.0V (at V _{DD} =AV _{DD} =5.0V).
28	SE -	1	Sled error signal input. Input range 2.5V±1.0V (at V _{DD} =AV _{DD} =5.0V).
29	FE	ı	Focus error signal input. Input range 2.5V±1.0V (at V _{DD} =AV _{DD} =5.0V).
30	vc	1	Mid-point voltage input terminal.
31	FILO	0	Filter output for master PLL.
32	FILI	l	Filter input for master PLL.
33	PCO	0	Charge pump output for master PLL.
34	CLTV	l	VCO control voltage input for master.
35	AVss	_	Analog GND.
36	RFAC	ı	EFM signal input.
37	BIAS	1	Asymmetry circuit constant current input.
38	ASYI	- 1	Asymmetry compare voltage input.
39	ASYO	0	EFM full swing output. (L=V _{SS} , H=V _{DD}).
40	AV _{DD}		Analog power supply.
41	V _{DD}		Digital power supply.
42	ASYE	1	Asymmetry circuit ON/OFF (L=OFF, H=ON).
43	PSSL	1	Audio data output mode shifting input. L to serial output, H to parallel output.
44	WDCK	0	48-bit slot D/A interface. Word clock f=2Fs.
45	LRCK	0	48-bit slot D/A interface. LR clock f=Fs.
46	DA16	0	DA16 output at PSSL=1. Serial data of 48-bit slot at PSSL=0.
47	DA15	0	DA15 output at PSSL=1. Bit clock of 48-bit slot at PSSL=0.
48	DA14	0	DA14 output at PSSL=1. Serial data of 64-bit slot at PSSL=0.

No. Name	Terminal	Symbol	1/0	Terminal Function
59 DA12 O DA14 output at PSSL-1. CHR clock of 64-bit start at PSSL-0. 51 DA10 O DA11 output at PSSL-1. XLGR output at PSSL-0. 52 DA10 O DA10 output at PSSL-1. XLGR output at PSSL-0. 53 DA09 O DA09 output at PSSL-1. XLGR output at PSSL-0. 54 DA08 O DA09 output at PSSL-1. XLGR output at PSSL-0. 55 DA07 O DA07 output at PSSL-1. CSP Output at PSSL-0. 56 DA06 O DA09 output at PSSL-1. AND Output at PSSL-0. 57 DA05 O DA09 output at PSSL-1. MNTO output at PSSL-0. 58 DA04 O DA09 output at PSSL-1. MNTO output at PSSL-0. 59 DA03 O DA09 output at PSSL-1. MNTO output at PSSL-0. 60 DA02 O DA09 output at PSSL-1. MNTO output at PSSL-0. 61 DA01 O DA01 output at PSSL-1. MNTO output at PSSL-0. 83 XTAO O XTat oscillation circuit furties. 1.6.3944MPL riput. 84 XTSL 1 Xtal oscillation circuit furties. 1.6.394MPL riput. 1.6.394MPL riput. <td>No.</td> <td>Name</td> <td></td> <td></td>	No.	Name		
STATE STA	49	DA13	0	DA13 output at PSSL=1. Bit clock of 64-bit slot at PSSL=0.
S2	50	DA12	0	DA12 output at PSSL=1. LR clock of 64-bit slot at PSSL=0.
S3	51	DA11	0	DA11 output at PSSL=1. GTOP output at PSSL=0.
54 DA08 O DA08 output at PSSL=1. RPGK output at PSSL=0. 55 DA07 O DA07 output at PSSL=1. RPGK output at PSSL=0. 56 DA06 O DA06 output at PSSL=1. JCRPO output at PSSL=0. 57 DA05 O DA06 output at PSSL=1. JKRAO output at PSSL=0. 58 DA04 O DA08 output at PSSL=1. MNT3 output at PSSL=0. 59 DA03 O DA02 output at PSSL=1. MNT1 output at PSSL=0. 60 DA02 O DA02 output at PSSL=1. MNT1 output at PSSL=0. 61 DA01 O DA01 output at PSSL=1. MNT1 output at PSSL=0. 62 XTAI 1 Xhis oscillation crimal input 1.5 s9444MHz or S3 8688MHz input. 63 XTAO O Xhis oscillation crimal toutput. 64 XTSL 1 X is election input terminal. L at Xhal for 16.9344MHz, at 33.8688MHz input. 65 Vis — Digital GND. 66 FSTT 1 22 divided input of terminals 62 and 63. Urwayring by variable pitched. 67 FSTG O 23 divided input of terminals 62 and 63. Urwayring by v	52	DA10	0	DA10 output at PSSL=1. XUGF output at PSSL=0.
65 DA07 O DA07 output at PSSL=1. RPCK output at PSSL=0. 66 DA06 O DA06 output at PSSL=1. CBPO output at PSSL=0. 57 DA06 O DA06 output at PSSL=1. RNAPO* output at PSSL=0. 58 DA04 O DA04 output at PSSL=1. MNT3 output at PSSL=0. 59 DA03 O DA02 output at PSSL=1. MNT3 output at PSSL=0. 60 DA02 O DA02 output at PSSL=1. MNT3 output at PSSL=0. 61 DA01 O DA02 output at PSSL=1. MNT3 output at PSSL=0. 62 XTA1 I Xxta coelitation circuit output. 63 XTAO O Xxta coelitation circuit output. 64 XTSL I Xxta coelitation circuit output. 65 Yxs — Digital GND. 66 FSTT I 22 divided input of terminals 62 and 63. Unwaying by variable pitche. 67 FSTO O 22 divided input of terminals 62 and 63. Unwaying by variable pitched. 68 C1M O 4.2866MHz output. Simultaneously varies when variable pitched. 69 C1EMP	53	DA09	0	DA09 output at PSSL=1. XPLCK output at PSSL=0.
56 DA06 O DA06 output at PSSL=1. C2PO output at PSSL=0. 57 DA05 O DA05 output at PSSL=1. MXP3 output at PSSL=0. 58 DA04 O DA04 output at PSSL=1. MXP3 output at PSSL=0. 59 DA03 O DA03 output at PSSL=1. MXP3 output at PSSL=0. 60 DA02 O DA07 output at PSSL=1. MXP1 output at PSSL=0. 61 DA01 O DA01 output at PSSL=1. MXP1 output at PSSL=0. 62 XTAI I Xtal socialisation circuit cutput. 63 XTAO O Xtal socialisation circuit cutput. 64 XTSL I Xtal socialisation circuit cutput. 65 Vs. — Digital onto. 66 PSTI I 23 divided input of terminals 82 and 63. 67 PSTO O 23 divided input of terminals 82 and 63. 68 CAM O 4.2858Mtx output. 69 C16M O 1.6944Mtx output. 70 MD2 I Digital-out ONIOFF control terminal (L=OFF, H=ON). 71 <td>54</td> <td>DA08</td> <td>0</td> <td>DA08 output at PSSL=1. GFS output at PSSL=0.</td>	54	DA08	0	DA08 output at PSSL=1. GFS output at PSSL=0.
57 DA05 O DA05 output at PSSL=1, XRAOF output at PSSL=0. 58 DA04 O DA04 output at PSSL=1. MIXT output at PSSL=0. 59 DA03 O DA03 output at PSSL=1. MIXT output at PSSL=0. 60 DA02 O DA02 output at PSSL=1. MIXT output at PSSL=0. 61 DA01 O DA01 output at PSSL=1. MIXT output at PSSL=0. 61 DA01 O DA01 output at PSSL=1. MIXT output at PSSL=0. 63 XTAO O X1al sealedton forcul to utput. 63 XTAO O X1al sealedton forcul to utput. 64 XTSL 1 X1al sealedton forcul tremminal. Lat X1al for 16.9344MHz, at 33.8688MHz turns to H. 65 Vsa — Digital and D. 66 FSTT 1 23 divided input of terminals 82 and 63. Unvarying by variable pitch. 67 FSTO 0 23 divided input of terminals (LaOFE, HE-DN). 70 MD2 1 Digital-out Output. Simultaneously varies when variable pitched. 69 C16M 0 16.9344MHz output. Simultaneously varies when variable pitched.	55	DA07	0	DA07 output at PSSL=1. RFCK output at PSSL=0.
S8	56	DA06	0	DA06 output at PSSL=1. C2PO output at PSSL=0.
59 DA03 O DA02 output at PSSL=1. MNT2 output at PSSL=0. 60 DA02 O DA02 output at PSSL=1. MNT3 output at PSSL=0. 61 DA01 O DA01 output at PSSL=1. MNT3 output at PSSL=0. 62 XTAI 1 Xtal oscillation circuit input. 16.8344MHz or 33.8688MHz input. 63 XTAO O Xtal oscillation circuit output. 64 XTSL 1 Xtal selection input terminal. L at Xtal for 16.9344MHz, at 33.8688MHz turns to H. 65 Vas — Digital GND. 66 FSTT 1 23 divided input of terminals 62 and 63. 67 FSTO 0 22 divided input of terminals 62 and 63. 68 C4M 0 4.2366MHz output. Simultaneously varies when variable pitched. 69 C16M 0 16.8344MHz output. Simultaneously varies when variable pitched. 70 MD2 1 Digital-out output terminal. 71 DOUT 0 Digital-out output terminal. 72 EMPH 0 Emphasis mode output deminal. (1 + a vital output. 7	57	DA05	0	DA05 output at PSSL=1. XRAOF output at PSSL=0.
60 DA02	58	DA04	0	DA04 output at PSSL=1. MNT3 output at PSSL=0.
61 DA01	59	DA03	0	DA03 output at PSSL=1. MNT2 output at PSSL=0.
62 XTAI I X'tal oscillation circuit input, 16.9344MHz or 33.8688MHz input. 63 XTAO O Xtal oscillation circuit output. 64 XTSL I Xtal selection input terminal. L at X'tal for 16.9344MHz, at 33.8688MHz turns to H. 65 Vss — Digital GND. 66 FSTI I 23 divided input of terminals 62 and 63. 67 FSTO O 23 divided input of terminals 62 and 63. Unvarying by variable pitch. 68 C4M O 4.2366MHz output. Simultaneously varies when variable pitched. 69 C18M O 16.9344MHz output. Simultaneously varies when variable pitched. 69 C18M O 16.9344MHz output. Simultaneously varies when variable pitched. 70 MD2 I Digital-out output iterminal (L=OFF, H=ON). 71 DOUT O Digital-out output of playback disc (L at without emphasis, H at emphasized). 73 WFCK O WFCK output. 74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Senal output of sub P−W. 76 EXCK I Clock input for SBSO read out. 77 SOSO O Subcode sync output. PCM peak data, level data 16-bit output. 78 SOCK I Clock input for SBSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRIC I Used for at Hrack jurnp. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data transfer clock input from CPU. 87 ATSK I Anti-shock terminal. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Voo — Digital power supply. 91 MiRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Servo control of spindle motor. 94 NDS O Servo control of spindle motor. 95 MON O Output filler shifting output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	60	DA02	0	DA02 output at PSSL=1. MNT1 output at PSSL=0.
63 XTAO O X'tal oscillation circuit output. 64 XTSL I Xtal selection input terminal. Lat X'tal for 16.9344MHz, at 33.8688MHz turns to H. 65 Vss — Digital GND. 66 FSTI I 23 divided input of terminals 62 and 63. 67 FSTO O 23 divided input of terminals 62 and 63. 68 CM O 4.3366MHz output. Simultaneously varies when variable pitched. 69 C16M O 16.9344MHz output. Simultaneously varies when variable pitched. 70 MD2 I Digital-out ON/OFF control terminal (L=OFF, H=ON). 71 DOUT O Digital-out output terminal. 72 EMPH O Emphasis mode output of playback disc (L at without emphasis, H at emphasized). 73 WFCK O WFCK output. 74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Serial output of sub P−W. 76 EXCK I Clock input for SBSO read out. 77 SQSO O Subc 80-bit output. PCM peak data, level data 16-bit output. 80 SENS O SENS Output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at Harck jump. 83 SCLK I Clock for at Harck jump. 84 DFSW I DeCT shifting terminal (H to mute). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 FSW O Defect signal output. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 SENS O Defect signal output. 94 FSW O Dutput files shifting output of spindle motor. 95 MON O Servo control of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	61	DA01	0	DA01 output at PSSL=1. MNT0 output at PSSL=0.
64 XTSL I X'tal selection input terminal. Lat X'tal for 16.9344MHz, at 33.8688MHz turns to H. 65 Vss — Digital GND. 66 FSTI I 23 divided input of terminals 62 and 63. 67 FSTO O 23 divided input of terminals 62 and 63. 67 FSTO O 23 divided input of terminals 62 and 63. 68 C4M O 4.2368MHz output. Simultaneously varies when variable pitched. 69 C16M O 16.9344MHz output. Simultaneously varies when variable pitched. 69 C16M O 16.9344MHz output. Simultaneously varies when variable pitched. 69 C16M O 16.9344MHz output. Simultaneously varies when variable pitched. 70 MD2 I Digital-out ONOFF control terminal (L=OFF, H=ON). 71 DOUT O Digital-out output terminal. 72 EMPH O Emphasis mode output of playback disc (L at without emphasis, H at emphasized). 73 WFCK O WFCK output. 74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SSSO O Subcode sync output for SSSO read out. 77 SQSO O Subcode Sync output for SSSO read out. 78 SQSO O Subcode Sync output for SSSO read out. 79 MUTE I Mute Shifting terminal (H to mute). 80 SENS O SENS Output Output s to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at I-track jump. 83 SCLK I Clock input for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anfi-shock terminal. 86 DATA I Serial data transfer clock input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vpo — Digital power supply. 91 MIRR O Mirror signal output. 94 FSW O Output filer shifting output of spindle motor. 95 MON O Servo control of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	62	XTAI	ı	X'tal oscillation circuit input. 16.9344MHz or 33.8688MHz input.
65 Vss	63	XTAO	0	X'tal oscillation circuit output.
66 FSTI I 2/3 divided input of terminals 62 and 63. Unvarying by variable pitch. 67 FSTO O 2/3 divided input of terminals 62 and 63. Unvarying by variable pitch. 68 C4M O 4.2366MHz output. Simultaneously varies when variable pitched. 69 C16M O 16.9344MHz output. Simultaneously varies when variable pitched. 70 MD2 I Digital-out ON/OFF control terminal (L=OFF, H=ON). 71 DOUT O Digital-out output terminal. 72 EMPH O Emphasis mode output of playback disc (L at without emphasis, H at emphasized). 73 WFCK O WFCK output. 74 SCOR O Subcode sync output eminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Senial output of sub P-W. 76 EXCK I Clock input for SBSO read out. 77 SQSO O Sub QS bett output. PCM peak data, level data 16-bit output. 78 SQCK I Clock input for SQSO read out. 79 MUTE I Mute shifting terminal (H to o	64	XTSL	1	X'tal selection input terminal. L at X'tal for 16.9344MHz, at 33.8688MHz turns to H.
67 FSTO O 2/3 divided input of terminals 62 and 63. Univarying by variable pitch. 68 C4M O 4.2366MHz output. Simultaneously varies when variable pitched. 69 C16M O 16.8344MHz output. Simultaneously varies when variable pitched. 70 MD2 I Digital-out ON/OFF control terminal (L=OFF, H=ON). 71 DOUT O Digital-out output terminal. 72 EMPH O Emphasis mode output of playback disc (L at without emphasis, H at emphasized). 73 WFCK O WFCK output. 74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Serial output of sub P~W. 76 EXCK I Clock input for SBSO read out. 77 SQSO O Sub0 80-bit output. PCM peak data, level data 16-bit output. 78 SQCK I Clock input for SDSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at I-track jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vob — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ONOFF control output of spindle motor. 96 MDP O Servo control of spindle motor.	65	Vss		Digital GND.
68 C4M O 4.2366MHz output. Simultaneously varies when variable pitched. 69 C16M O 16.9344MHz output. Simultaneously varies when variable pitched. 70 MD2 I Digital-out ON/OFF control terminal (L=OFF, H=ON). 71 DOUT O Digital-out output terminal. 72 EMPH O Emphasis mode output of playback disc (L at without emphasis, H at emphasized). 73 WFCK O WFCK output. 74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Serial output of sub P-W. 76 EXCK I Clock input for SBSO read out. 77 SQSO O SubQ 80-bit output. PCM peak data, level data 16-bit output. 78 SQCK I Clock input for SQSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at H-rack jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Voo — Digital power supply. 91 MIRR O Mirror signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ONOFF control output of spindle motor. 96 MDP O Servo control of spindle motor.	66	FSTI	ı	2/3 divided input of terminals 62 and 63.
69 C16M O 16.9344MHz output. Simultaneously varies when variable pitched. 70 MD2 I Digital-out ON/OFF control terminal (L=OFF, H=ON). 71 DOUT O Digital-out output terminal. 72 EMPH O Emphasis mode output of playback disc (L at without emphasis, H at emphasized). 73 WFCK O WFCK output. 74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Serial output of sub P=W. 76 EXCK I Clock input for SBSO read out. 77 SOSO O SubO 90-bit output. PCM peak data, level data 16-bit output. 78 SOCK I Clock input for SOSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS OUTPUT. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at Hrack jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Voo — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 94 FSW O Output filer shifting output of spindle motor. 95 MON O ON/OFF control of spindle motor. 96 MDP O Servo control of spindle motor.	67	FSTO	0	2/3 divided input of terminals 62 and 63. Unvarying by variable pitch.
70 MD2 I Digital-out ON/OFF control terminal (L=OFF, H=ON). 71 DOUT O Digital-out output terminal. 72 EMPH O Emphasis mode output of playback disc (L at without emphasis, H at emphasized). 73 WFCK O WFCK output. 74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Serial output of sub P=W. 76 EXCK I Clock input for SBSO read out. 77 SQSO O Subc 80-bit output. PCM peak data, level data 16-bit output. 78 SCOK I Clock input for SGSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS OUS SENS OUT	68	C4M	0	4.2366MHz output. Simultaneously varies when variable pitched.
71 DOUT O Digital-out output terminal. 72 EMPH O Emphasis mode output of playback disc (Lat without emphasis, H at emphasized). 73 WFCK O WFCK output. 74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Serial output of sub P-W. 76 EXCK I Clock input for SBSO read out. 77 SQSO O SubO 80-bit output. PCM peak data, level data 16-bit output. 78 SQCK I Clock input for SQSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at I-track jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vpp — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O Servo control of spindle motor. 96 MDP O Servo control of spindle motor.	69	C16M	0	16.9344MHz output. Simultaneously varies when variable pitched.
T2	70	MD2	I	Digital-out ON/OFF control terminal (L=OFF, H=ON).
73 WFCK O WFCK output. 74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Serial output of sub P-W. 76 EXCK I Clock input for SBSO read out. 77 SQSO O SubQ 80-bit output. PCM peak data, level data 16-bit output. 78 SQCK I Clock input for SQSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at I-track jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Voo — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	71	DOUT	0	Digital-out output terminal.
74 SCOR O Subcode sync output terminal (H at detecting either one of SO or SI subcode sync). 75 SBSO O Serial output of sub P-W. 76 EXCK I Clock input for SBSO read out. 77 SQSO O SubO 80-bit output. PCM peak data, level data 16-bit output. 78 SQCK I Clock input for SQSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS OUTPUT. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at H-rack jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal. (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vob — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	72	EMPH	0	Emphasis mode output of playback disc (L at without emphasis, H at emphasized).
75 SBSO O Serial output of sub P-W. 76 EXCK I Clock input for SBSO read out. 77 SQSO O SubQ 80-bit output. PCM peak data, level data 16-bit output. 78 SQCK I Clock input for SQSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at I-track jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vod — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ONOFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	73	WFCK	0	WFCK output.
76 EXCK I Clock Input for SBSO read out. 77 SQSO O SubQ 80-bit output. PCM peak data, level data 16-bit output. 78 SQCK I Clock input for SQSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at Hrack jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Voo — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	74	SCOR	0	Subcode sync output terminal (H at detecting either one of SO or SI subcode sync).
77 SGSO O SubQ 80-bit output. PCM peak data, level data 16-bit output. 78 SGCK I Clock input for SGSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at Hrack jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vop — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	75	SBSO	0	Serial output of sub P~W.
78 SQCK I Clock input for SQSO read out. 79 MUTE I Mute shifting terminal (H to mute). 80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at I-track jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 VpD — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	76	EXCK	ı	Clock input for SBSO read out.
79 MUTE	77	sqso	0	SubQ 80-bit output. PCM peak data, level data 16-bit output.
80 SENS O SENS output. Outputs to CPU. 81 XRST I System reset (L to reset). 82 DIRC I Used for at I-track jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vop — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	78	SQCK	ı	Clock input for SQSO read out.
81 XRST I System reset (L to reset). 82 DIRC I Used for at I-track jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vop — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	79	MUTE	1	Mute shifting terminal (H to mute).
82 DIRC I Used for at I-track jump. 83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vpb — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor.	80	SENS	0	SENS output. Outputs to CPU.
83 SCLK I Clock for SENS serial data reading. 84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vpp — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	81	XRST	1	System reset (L to reset).
84 DFSW I DFCT shifting terminal (H to DFCT countermeasure circuit OFF). 85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 VoD — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	82	DIRC	1	Used for at I-track jump.
85 ATSK I Anti-shock terminal. 86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 V _{DD} — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	83	SCLK	1	Clock for SENS serial data reading.
86 DATA I Serial data input from CPU. 87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vob — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	84	DFSW	L	DFCT shifting terminal (H to DFCT countermeasure circuit OFF).
87 XLAT I Latch input from CPU. 88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 VpD — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	85	ATSK	1	Anti-shock terminal.
88 CLOK I Serial data transfer clock input from CPU. 89 COUT O Number of track count signal output. 90 Vob — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	86	DATA	1	Serial data input from CPU.
89 COUT O Number of track count signal output. 90 V _{DD} — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	87	XLAT	1	Latch input from CPU.
90 V _{DD} — Digital power supply. 91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	88	CLOK	1	Serial data transfer clock input from CPU.
91 MIRR O Mirror signal output. 92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	89	COUT	0	Number of track count signal output.
92 DFCT O Defect signal output. 93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	90	V _{DD}	_	Digital power supply.
93 FOK O Focus OK output. 94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	91	MIRR	0	Mirror signal output.
94 FSW O Output filter shifting output of spindle motor. 95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	92	DFCT	0	Defect signal output.
95 MON O ON/OFF control output of spindle motor. 96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	93	FOK	0	Focus OK output.
96 MDP O Servo control of spindle motor. 97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	94	FSW	0	Output filter shifting output of spindle motor.
97 MDS O Servo control of spindle motor. 98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	95	MON	0	ON/OFF control output of spindle motor.
98 LOCK O Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.	96	MDP	0	Servo control of spindle motor.
	97	MDS	0	Servo control of spindle motor.
99 SSTP I Terminal for inner most circle detection signal of disc.	98	LOCK	0	Sampling GFS with 460Hz and outputs H at GFS is H. Outputs L when continuously 8 times L.
	99	SSTP		Terminal for inner most circle detection signal of disc.
100 SFDR O Sled drive output.	100	SFDR	0	Sled drive output.

Note: • 64-bit slot is 2's compliment output of LSB first, 48-bit slot is 2's compliment output of MSB first.

- GTOP is to monitor the protection condition of Frame Sync. (H: Sync protect window open.)
- XUGF is Frame Sync obtained from EFM signal and is negative pulse.
- XPLCK is reversal of EFM PLL clock. PLL is so made the rising edge to meet shifting point of EFM signal.
- GFS signal is a signal to turn to H when frame Sync and inserted protection timing coincide.
- RFCK is obtained with the accuracy of X'tal. The signal of 136µs cycle.
- C2PO is a signal to indicate the state of data error.
- XRAOF is a generating signal when 32kRAM exceeds ±28 frame jitter margin.



DOV

7 TH201 DTA143TS C802 D0 VM

to SGND

ско. 1 <u>Т</u>

to SGND

WARNING:
Parts marked with this symbol \(\frac{\hat{\Delta}}{\Delta}\)
Use ONLY replacement parts recomme
CAUTION:

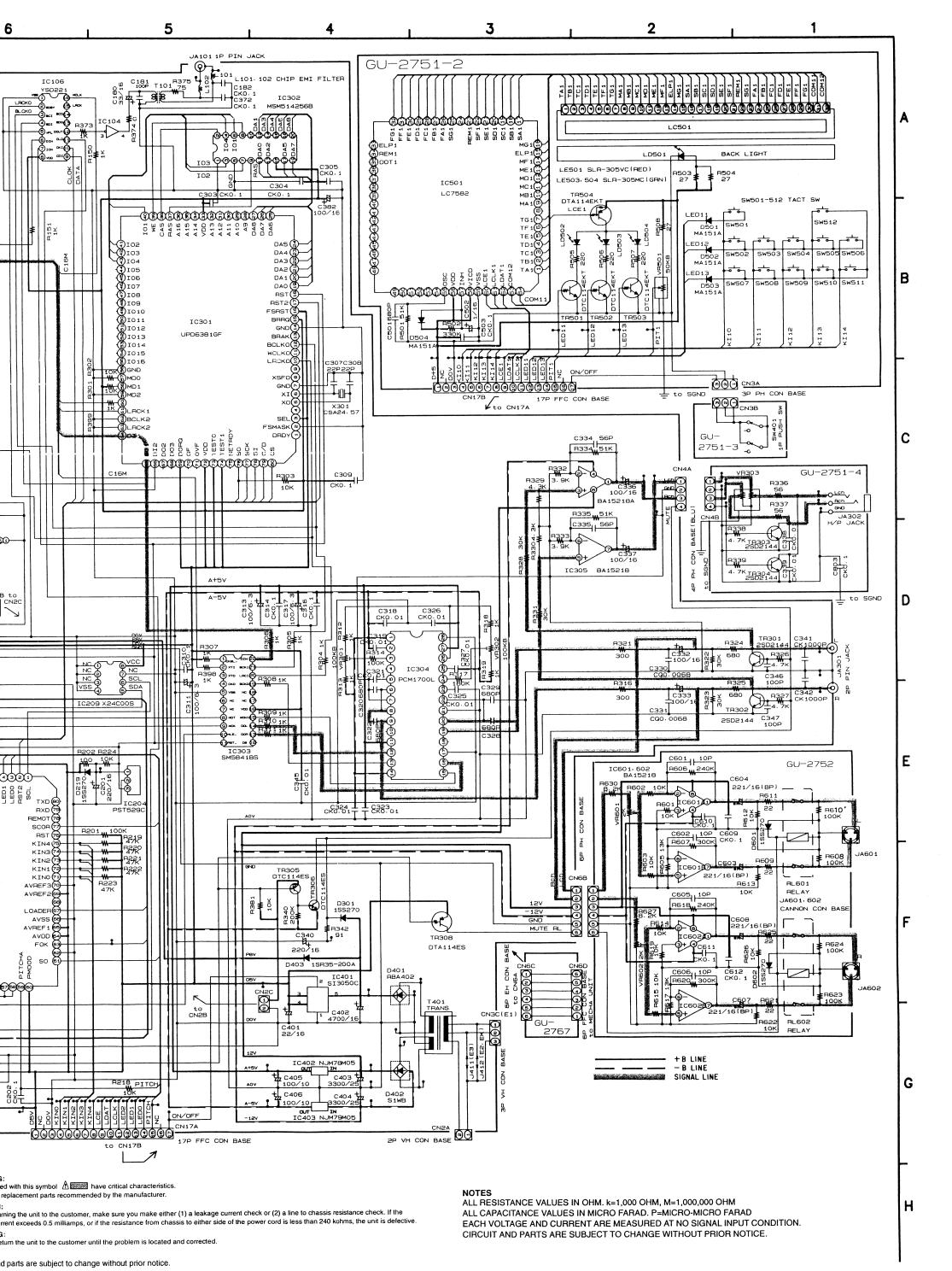
Before returning the unit to the custome leakage current exceeds 0.5 milliamps, WARNING:

C204 CKO. 1

C800 CKO: 1

는 to SGND

DO NOT return the unit to the custome NOTES:
Circuit and parts are subject to cha



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